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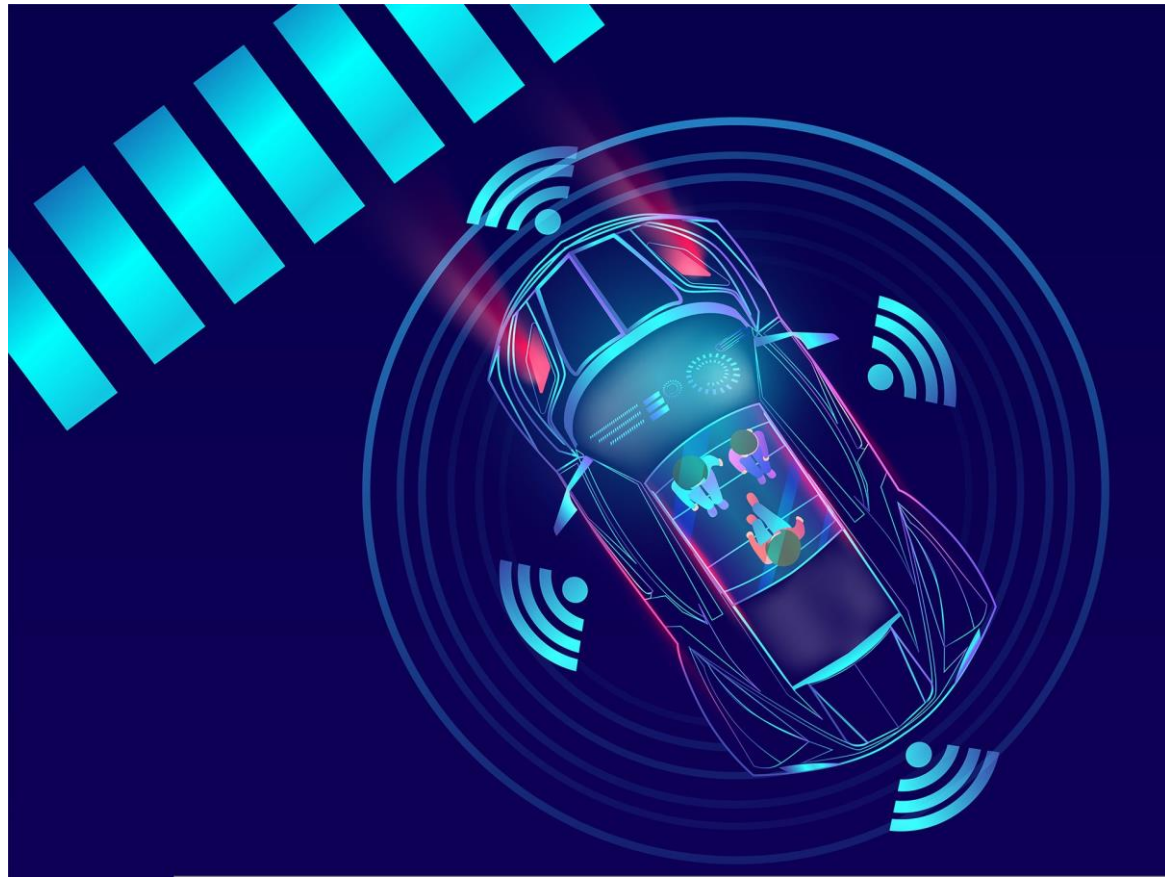
November 20th, 2025

MaxiMals: A Low-cost Hardening Technique for Large Vision Transformers

Lucas Roquet, Fernando Fernandes dos Santos, Paolo Rech, Marcello Traiola, Olivier Sentieys, and Angeliki Kritikakou

Machine Learning (ML) is Everywhere

TECHNOLOGY
CYBER
CYBER
CYBER
CYBERW
BERWE
RWE
JEEK

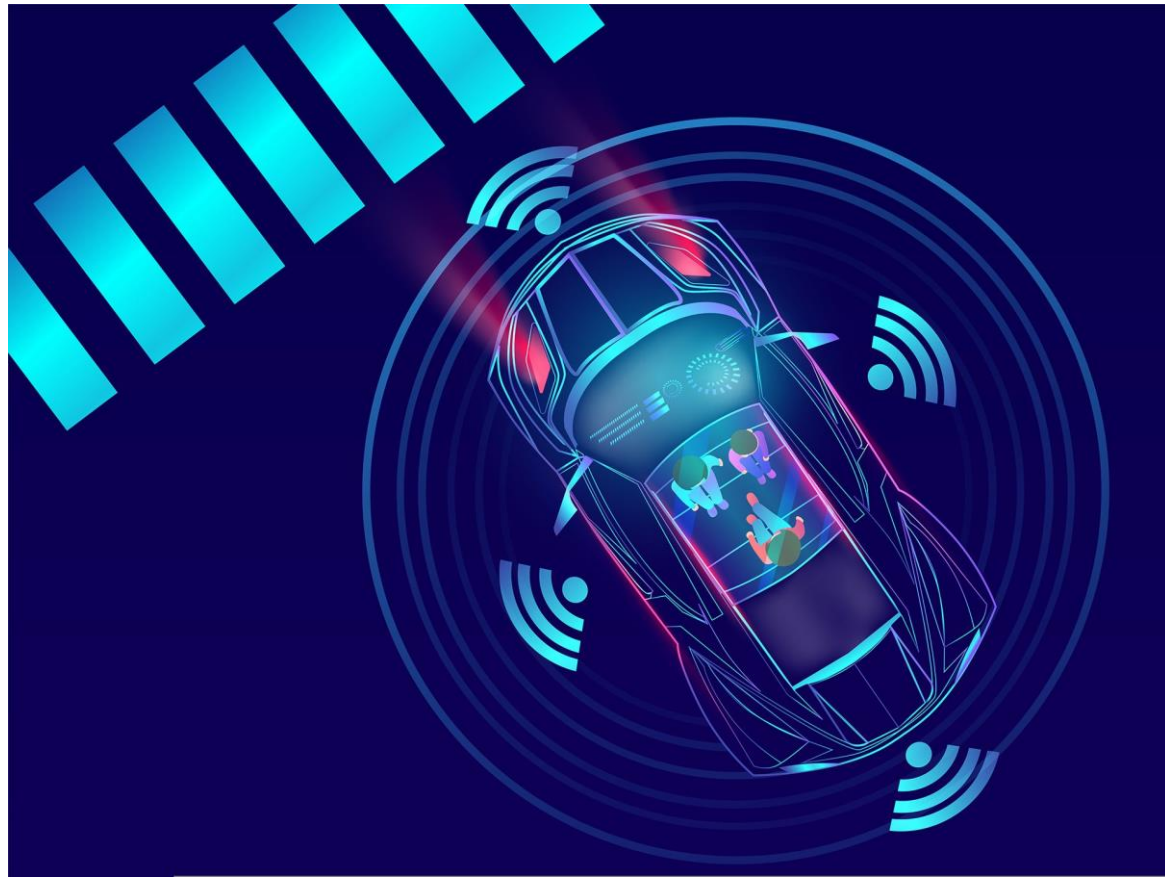


- ML models are very **complex** and very **accurate**
- They can be applied to a wide range of applications



Machine Learning (ML) is Everywhere

TECHNOLOGY
CYBER
CYBER
CYBER
CYBERW
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JEEK



**ML models are included in
Safety-critical Applications**

Transformer Models

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- Transformers, state-of-the-art of ML

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 - Highly accurate

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- Transformers, state-of-the-art of ML
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 - Various tasks : language processing, radar processing or...

Transformer Models

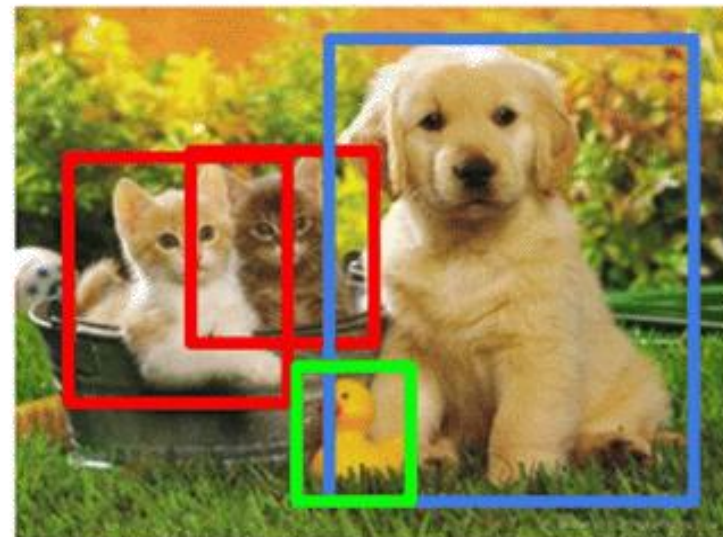
- Transformers, state-of-the-art of ML
 - Highly accurate
 - Various tasks : language processing, radar processing or...
 - ... computer vision (ViTs)

Classification



CAT

Object Detection

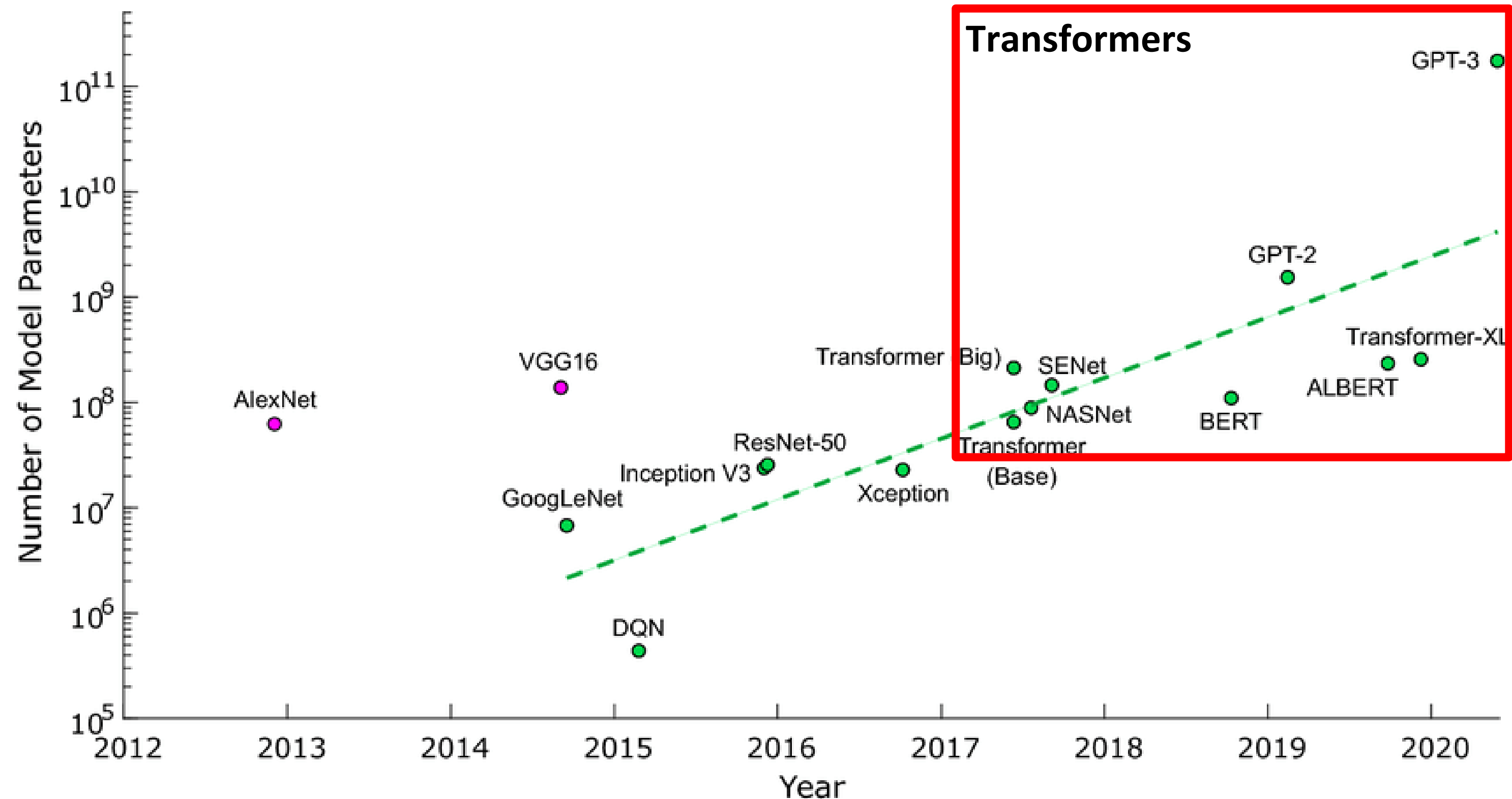


CAT, DOG, DUCK

Image Segmentation

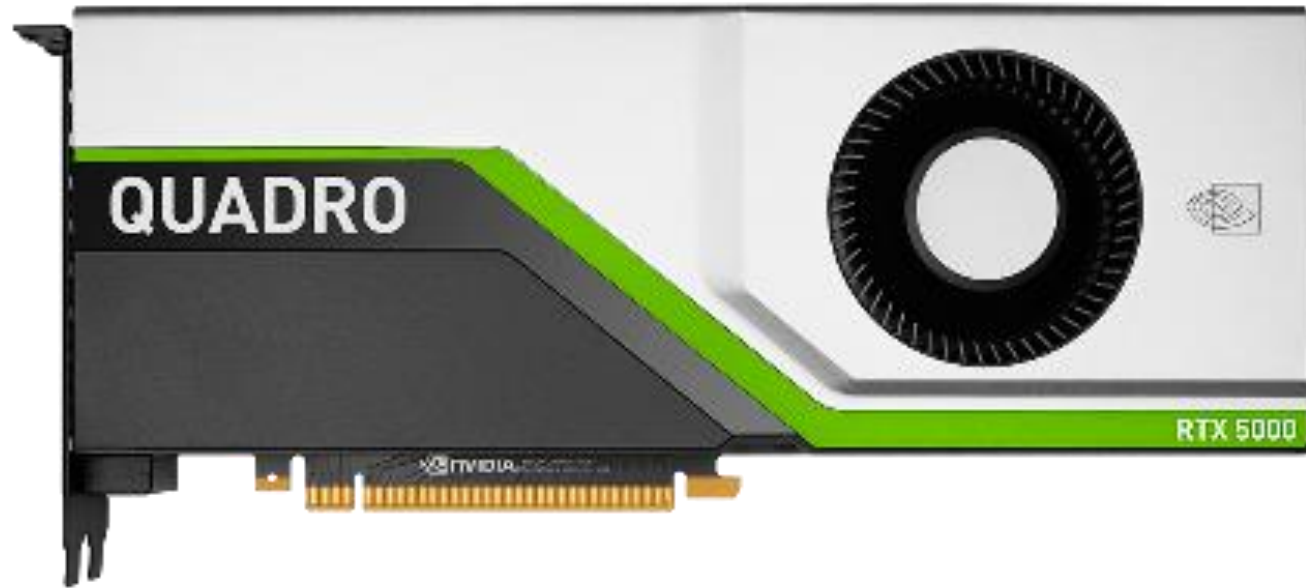


Transformer Models



Evolution of the number of parameters of ML models¹

Transformer Models



They need large hardware accelerators

Vulnerability Sources

The computer errors from outer space

12 October 2022

By Chris Baraniuk, Features correspondent

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Satellites orbiting the Earth, including the International Space Station, are particularly vulnerable to space weather (Credit: Nasa)

The Earth is subjected to a hail of subatomic particles from the Sun and beyond our solar system which could be the cause of glitches that afflict our phones and computers. And the risk is growing as microchip technology shrinks.

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Neutrons, Protons, Heavy-ions, etc.



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New Research Shows Potential of Electromagnetic Fault Injection Attacks Against Drones

New research conducted by IOActive shows the potential of electromagnetic fault injection (EMFI) attacks against drones.



By Eduard Kovacs
June 13, 2023



New research shows the potential of electromagnetic fault injection (EMFI) attacks against unmanned aerial vehicles, with experts showing how drones that don't have any known vulnerabilities could be hacked.

TRENDING

- 1 Rockwell Automation Urges Customers to Disconnect ICS From Internet
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- 3 Google Patches Fourth Chrome Zero-Day in Two Weeks
- 4 American Radio Relay League Hit by Cyberattack
- 5 Newly Detected Chinese Group Targeting Military, Government Entities
- 6 User Outcry as Slack Scrapes Customer Data for AI Model Training
- 7 400,000 Impacted by Centromed Data Breach
- 8 Zero-Day Attacks and Supply Chain Compromises Surge, MFA Remains Underutilized: Rapid7 Report



Vulnerability Sources

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Electromagnetic FI, laser FI, SCAs, etc.



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Vulnerability Sources

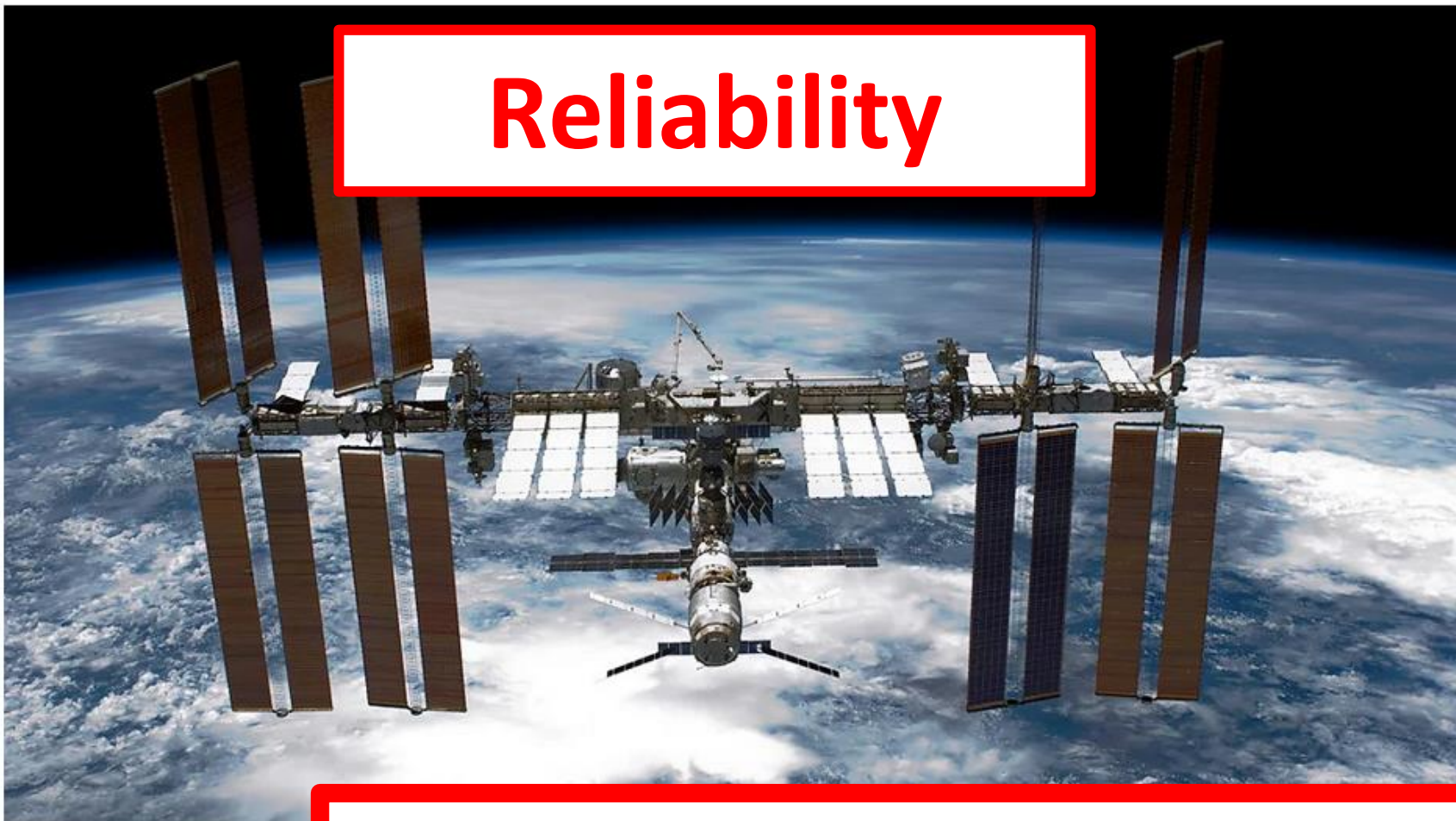
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They need protection mechanisms

any known vulnerabilities could be hacked.



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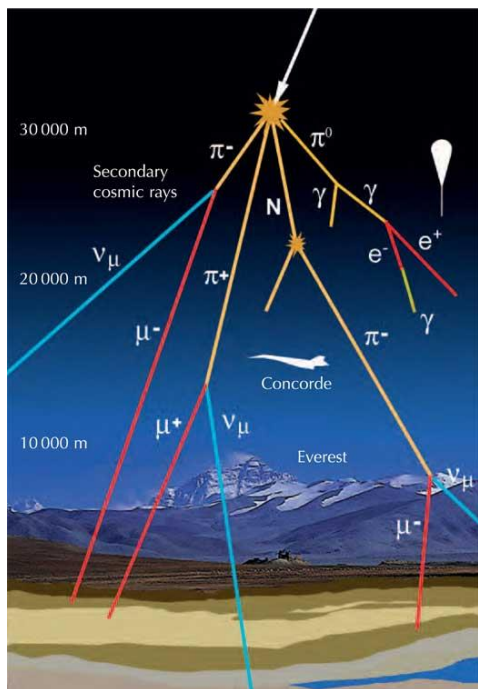


Agenda

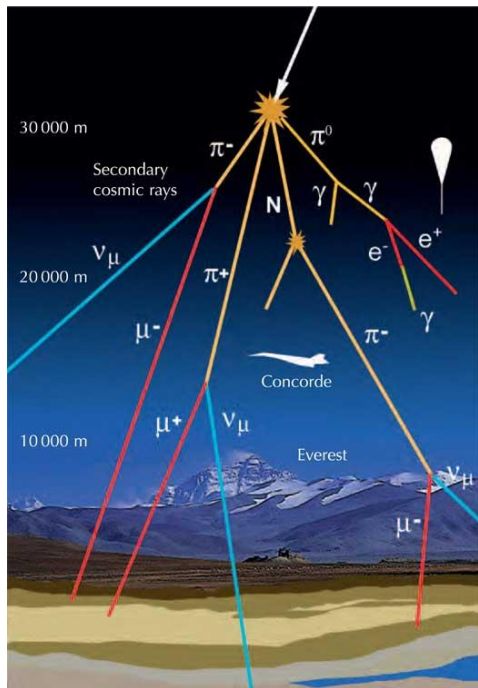
- **Radiation Effects on ML**
- **ViT Reliability Characterization**
- **Efficient Fault Tolerance**
- **Conclusions and Future Directions**

Radiation Effects on ML

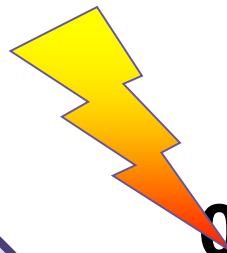
Radiation Impact on GPUs



Radiation Impact on GPUs

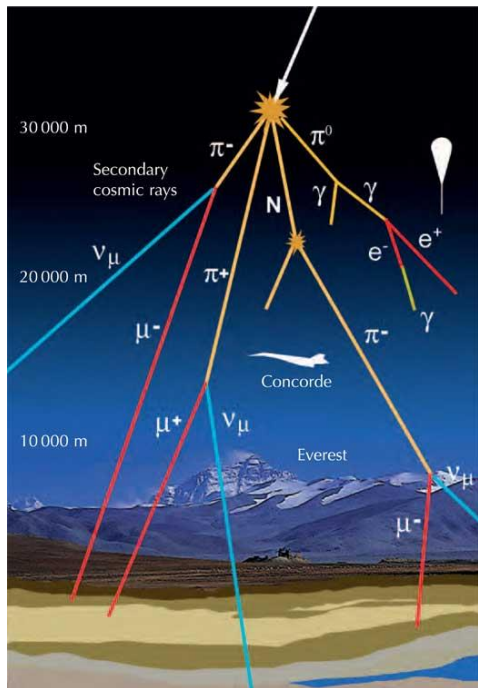


Memories



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Radiation Impact on GPUs



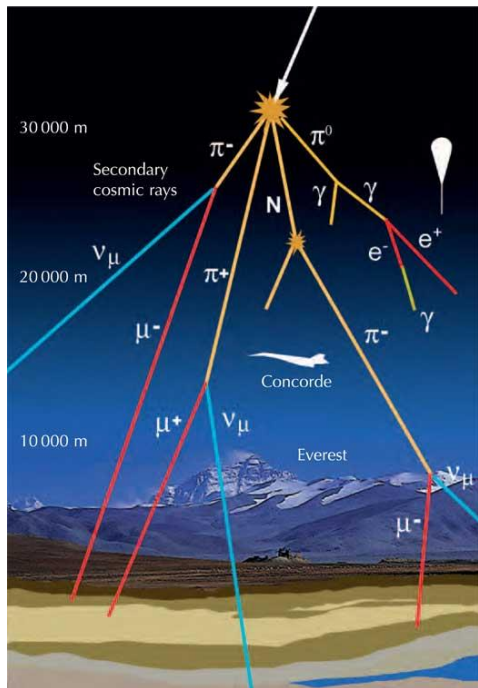
Memories

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Logic

1 + 1 = 3

Radiation Impact on GPUs



Memories

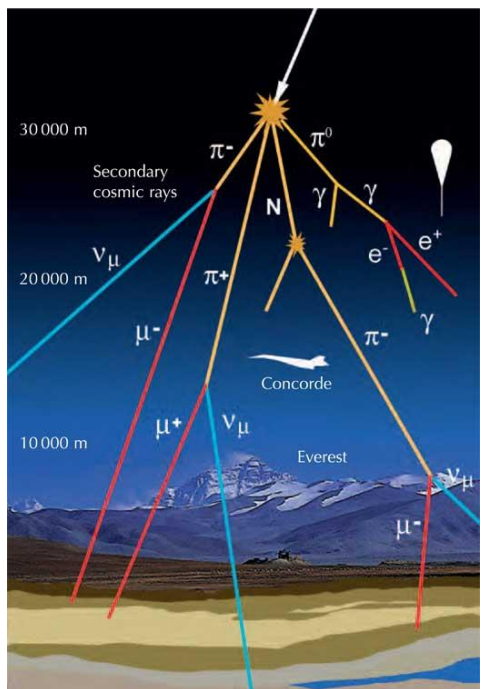
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Radiation Impact on GPUs



Memories

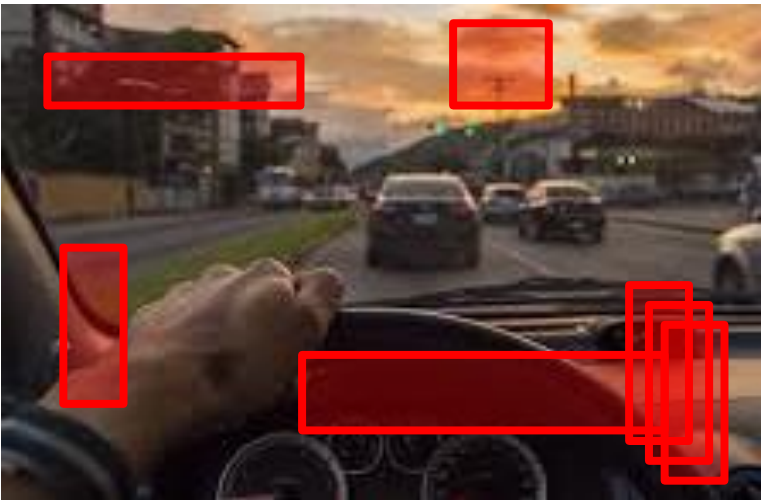
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Logic

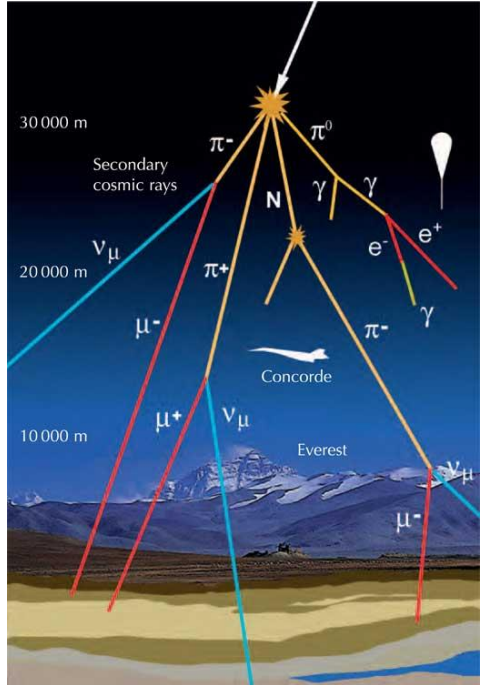
$$1 + 1 = 3$$

Silent Data Corruption (SDC)

- Computation is done but result is altered



Radiation Impact on GPUs



Memories

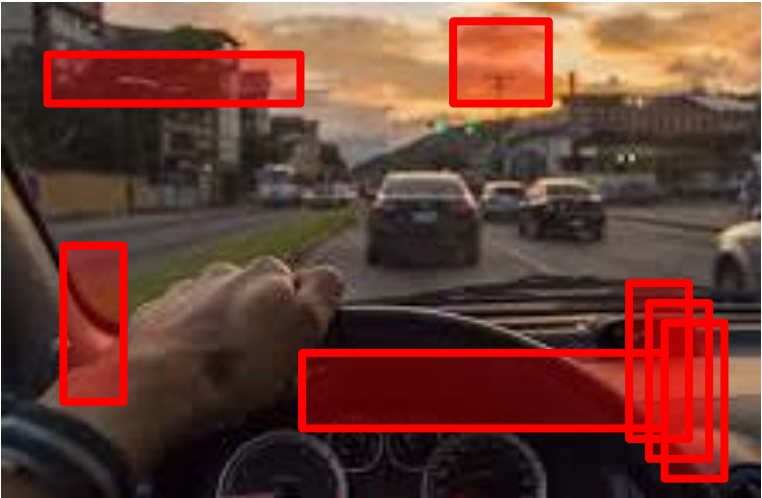
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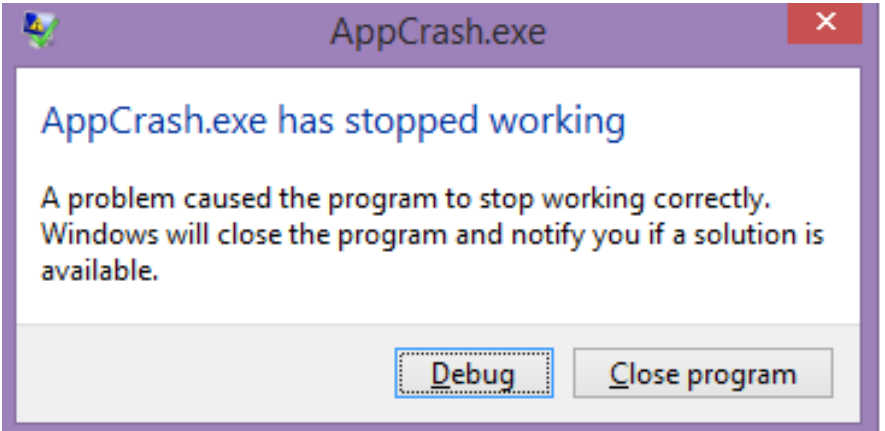
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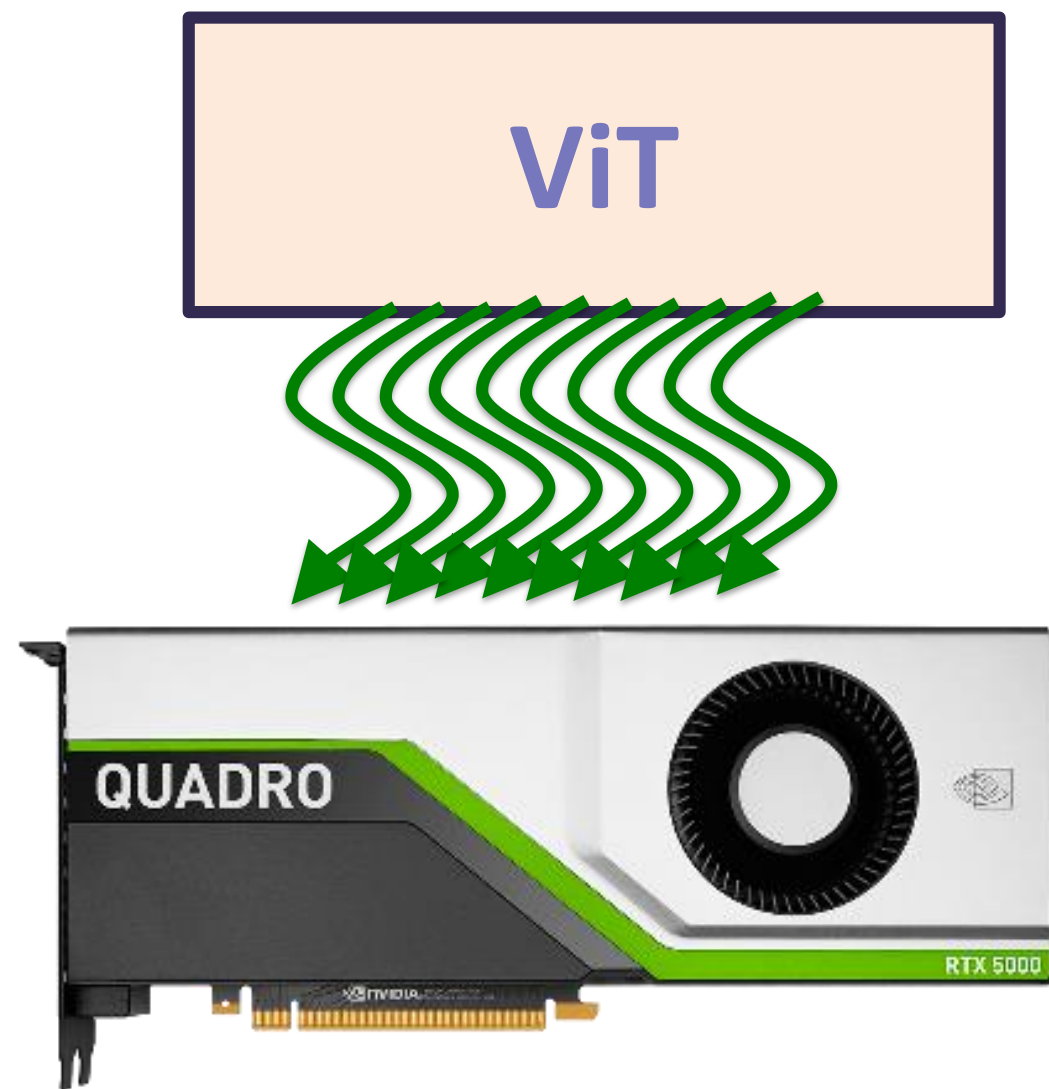


Data Unrecoverable Error (DUE)

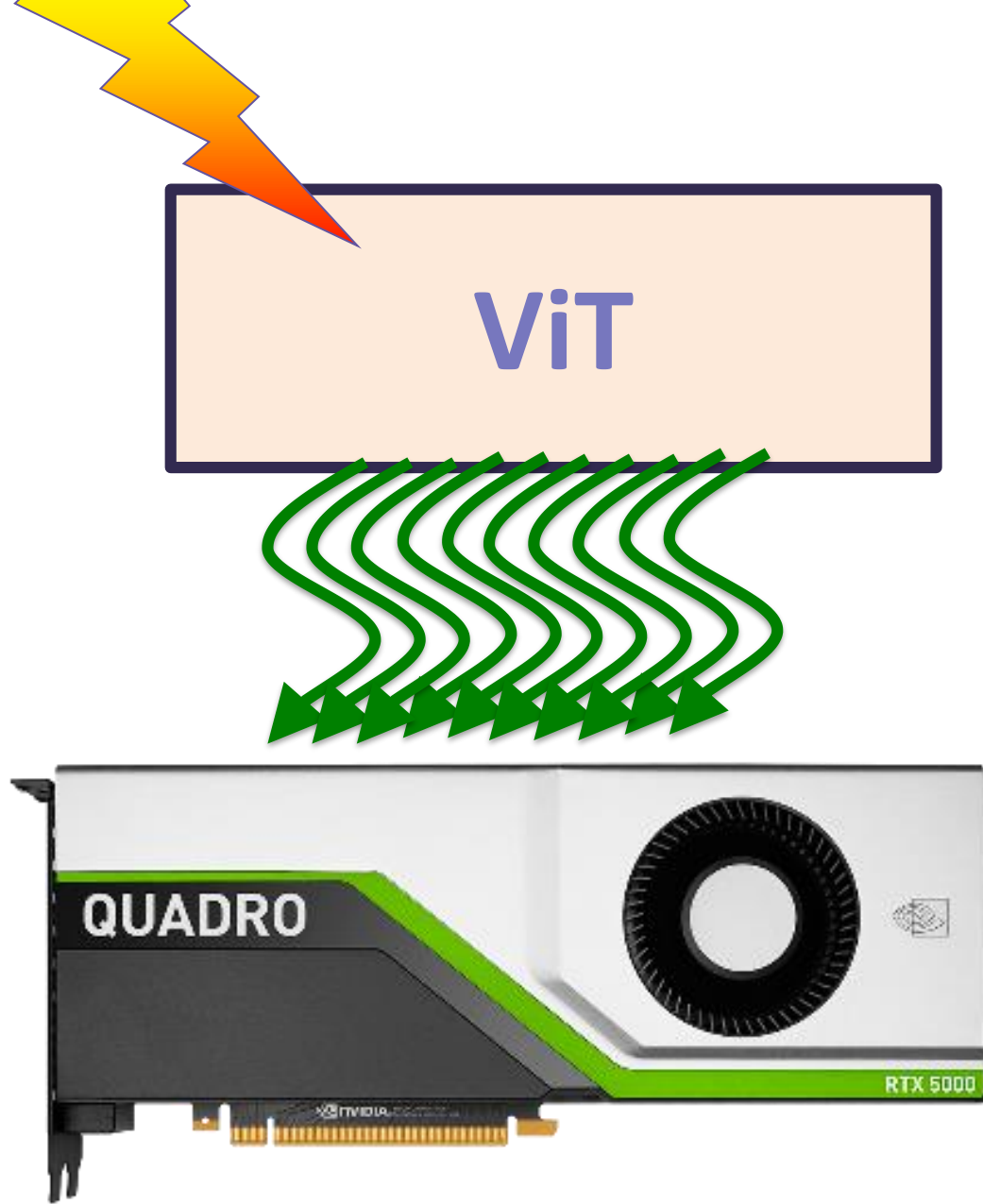
- Crash and OS hangs



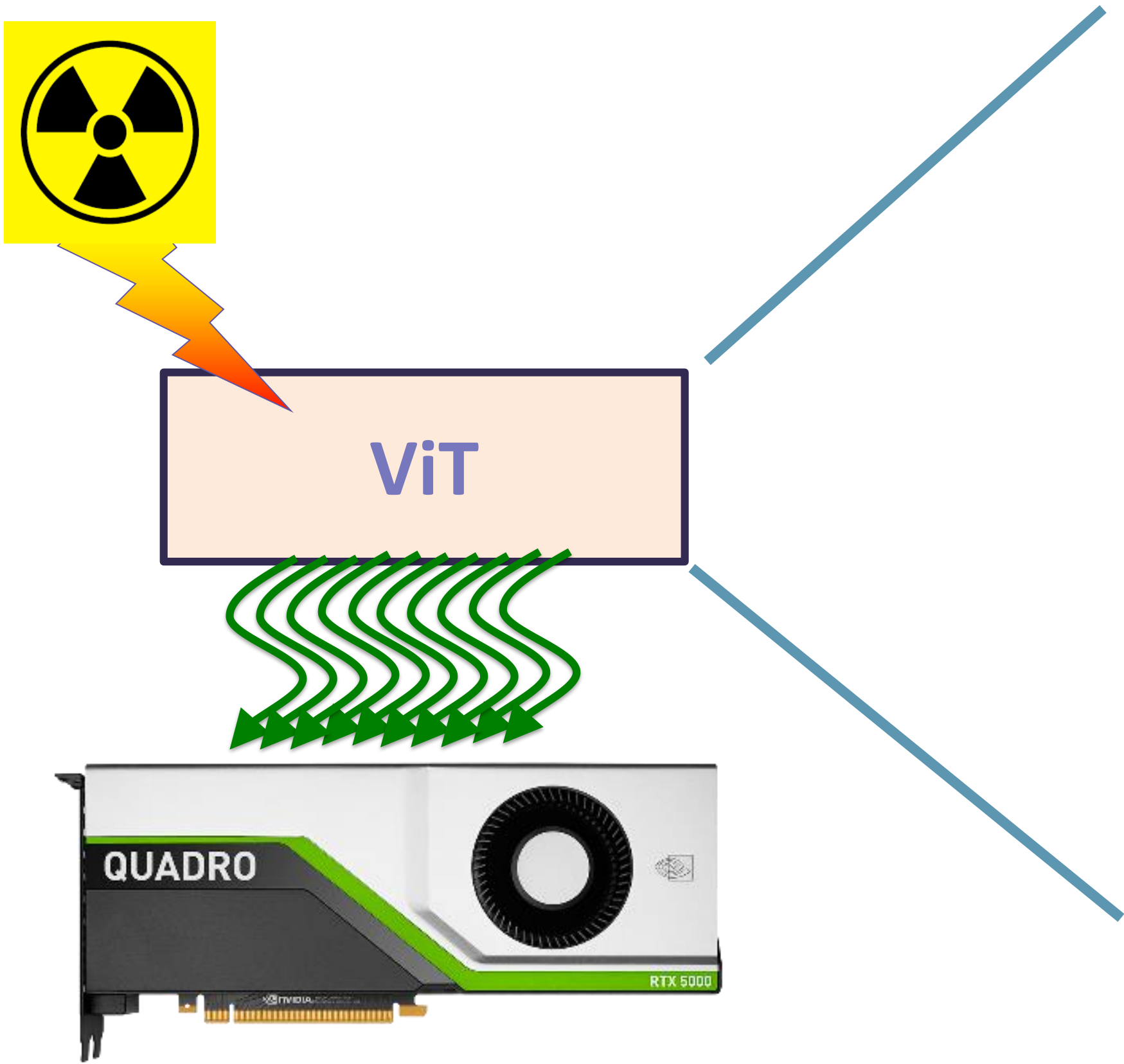
ViT SDC Types



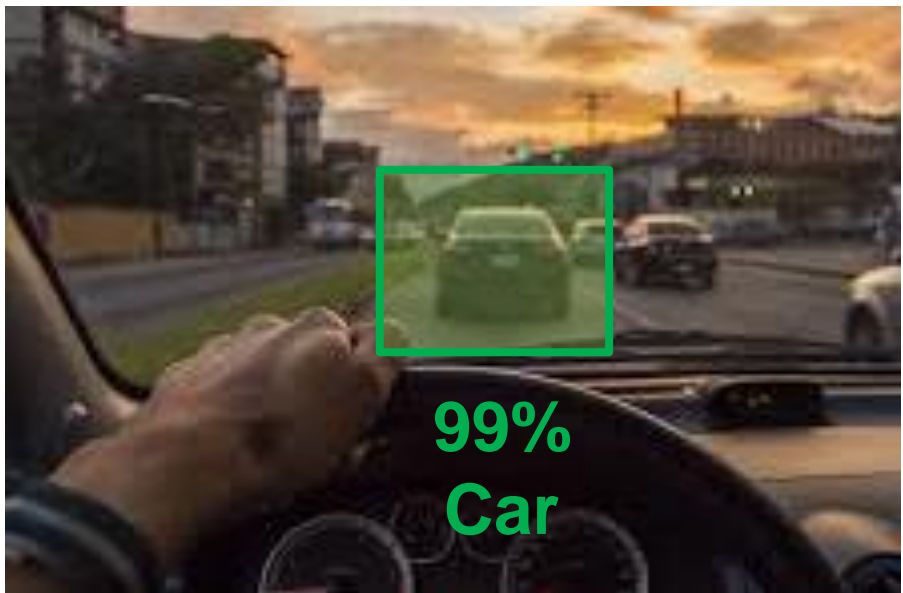
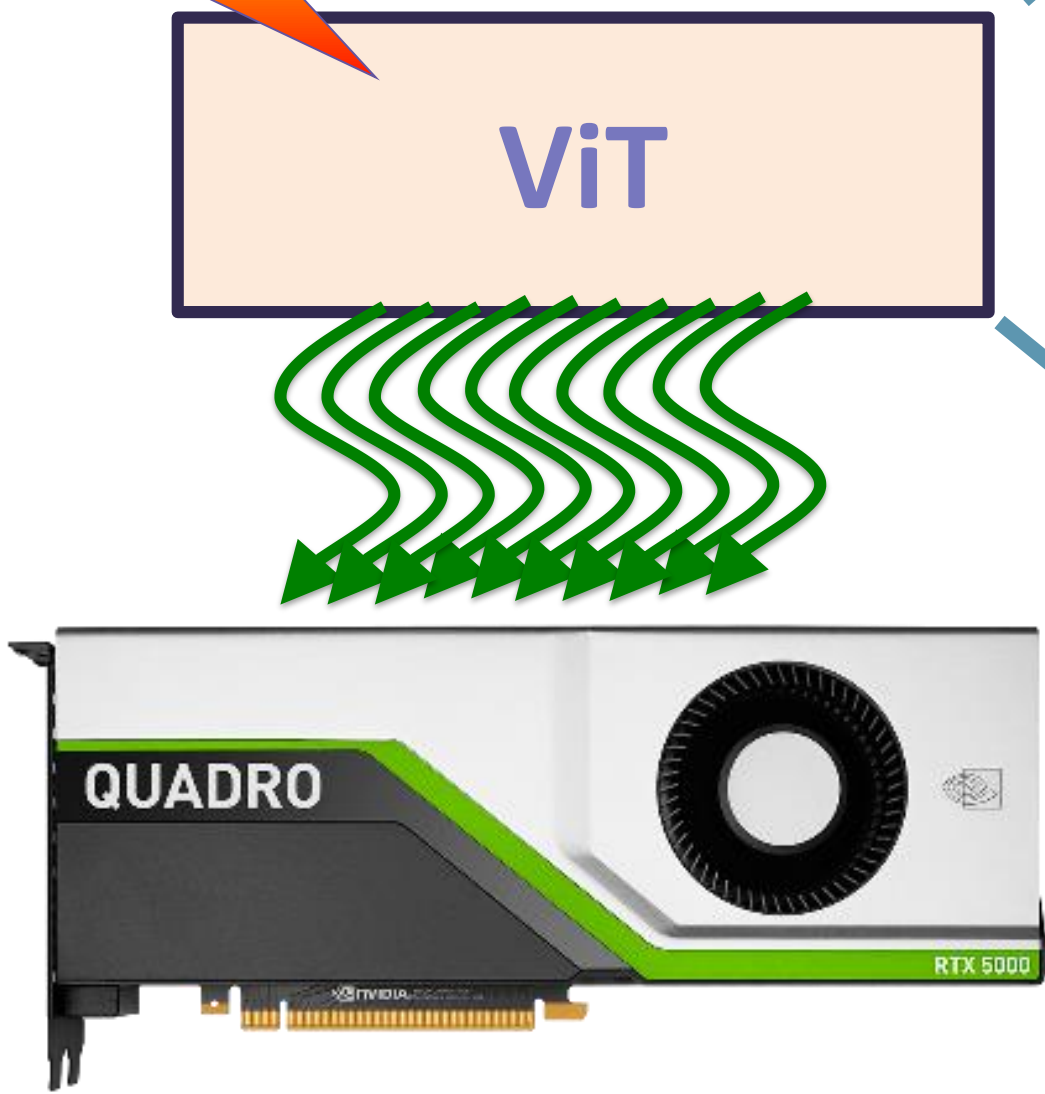
ViT SDC Types



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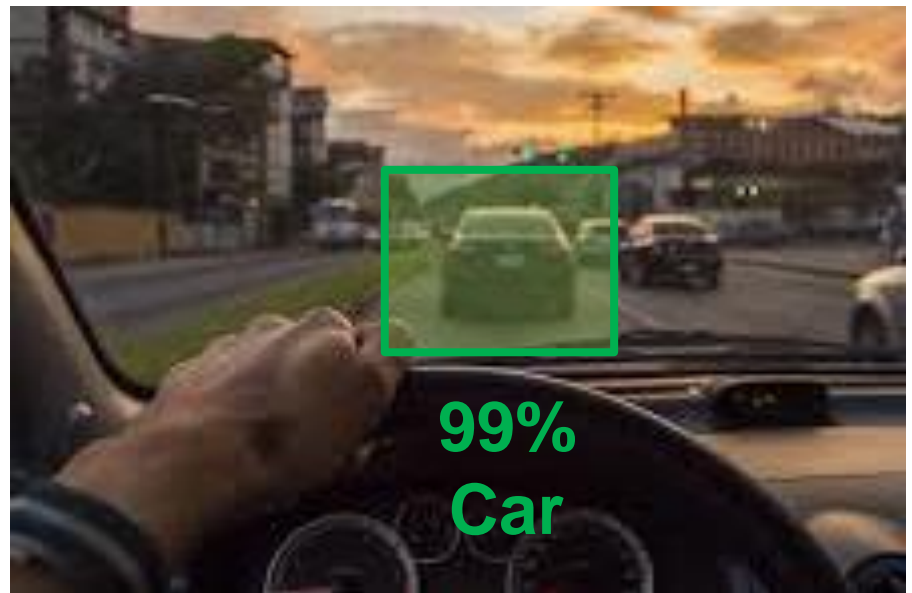
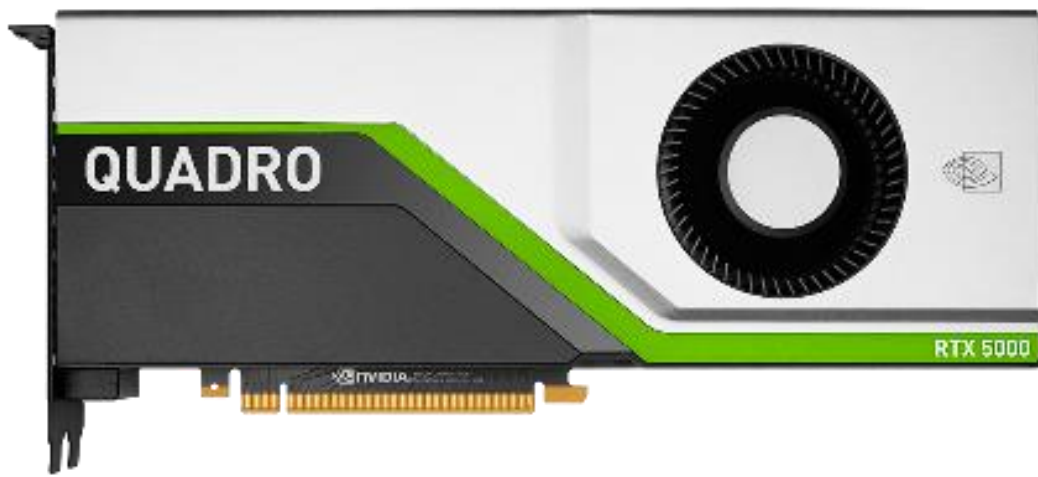


ViT SDC Types

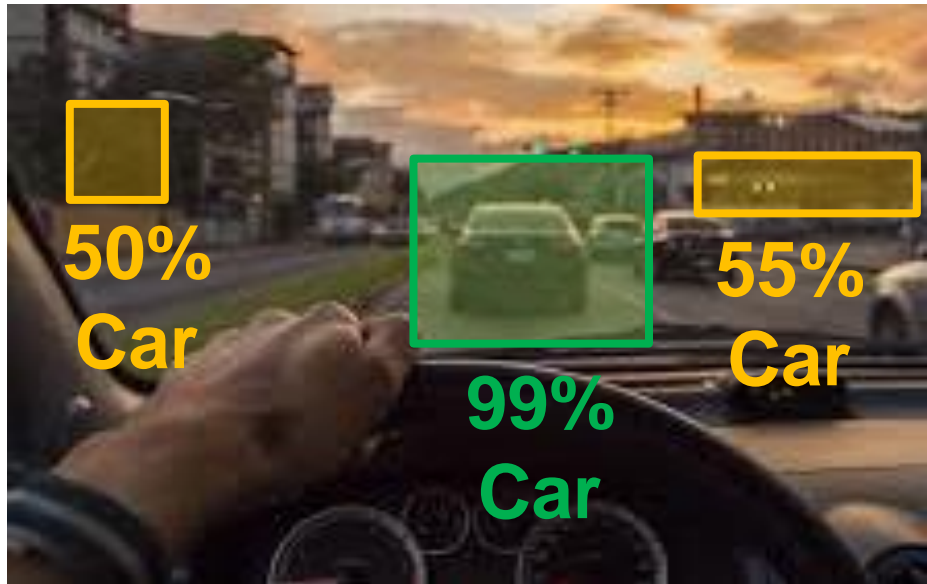


Masked

ViT SDC Types



Masked

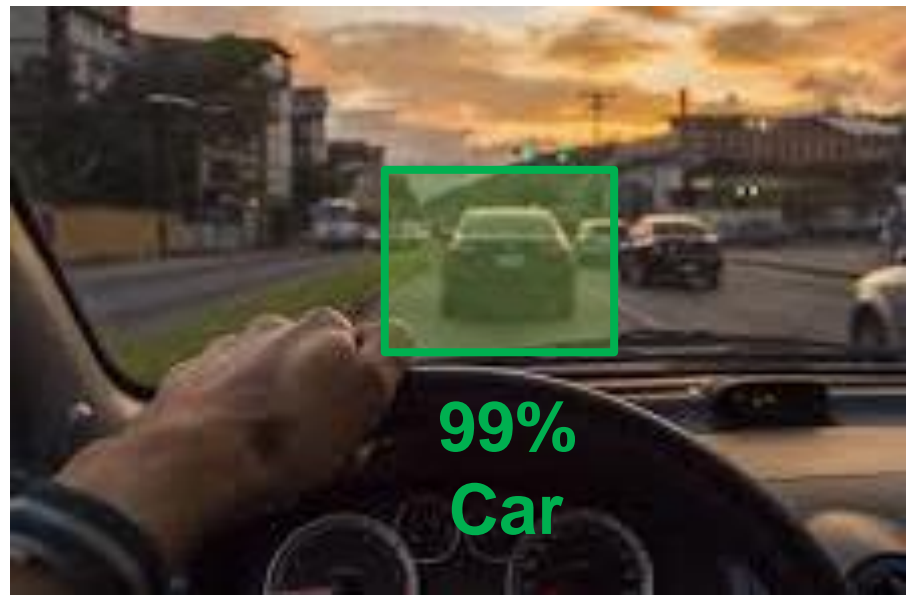
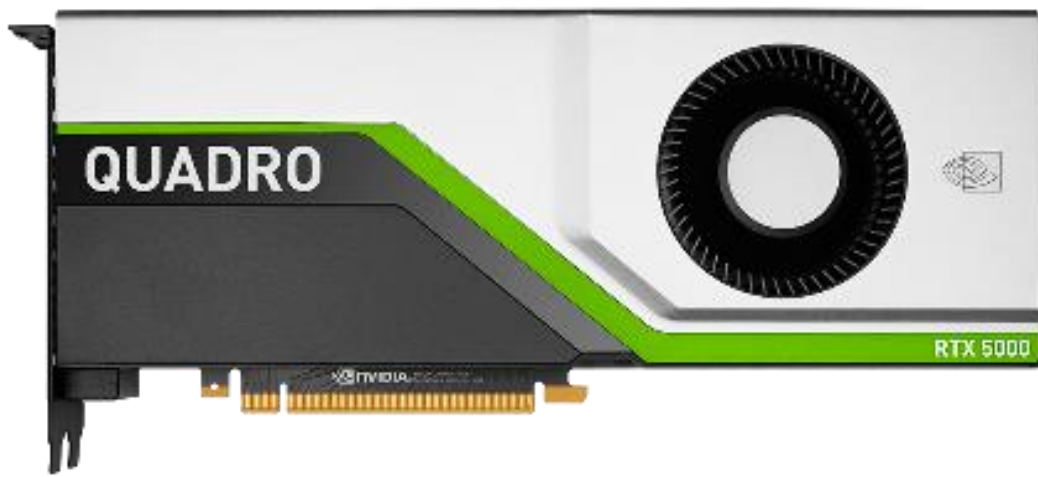
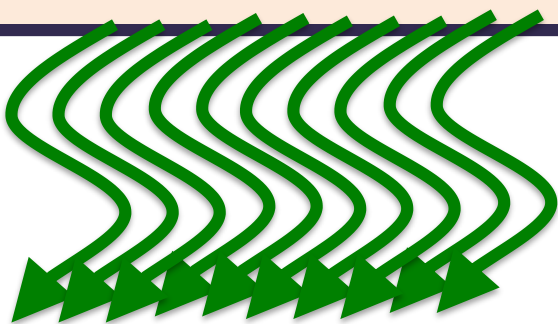


Tolerable SDC

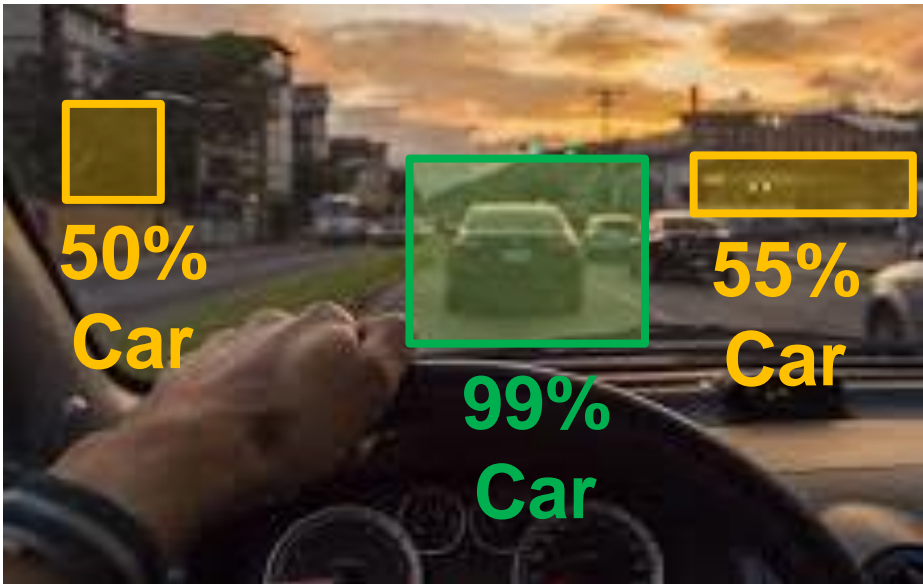
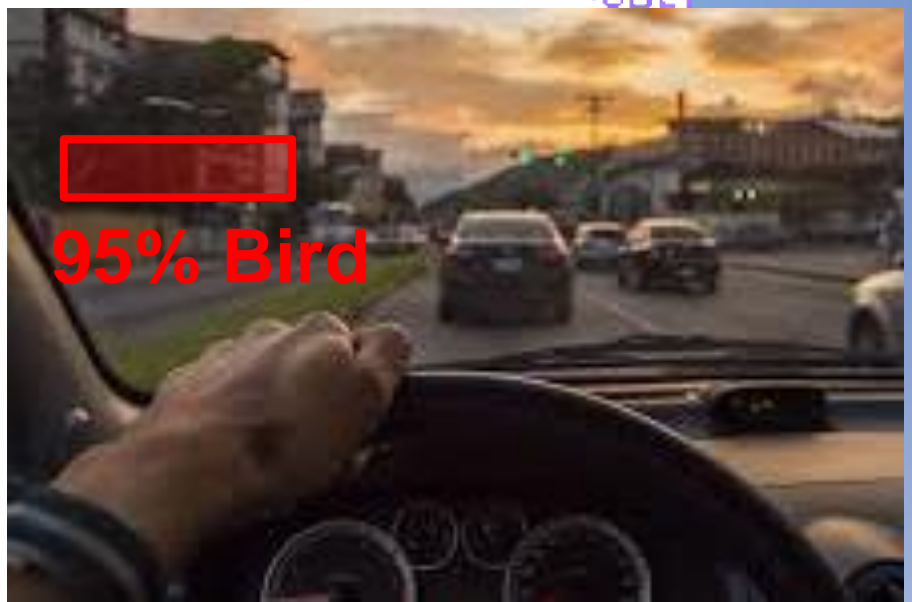
ViT SDC Types



ViT



Masked



Tolerable SDC

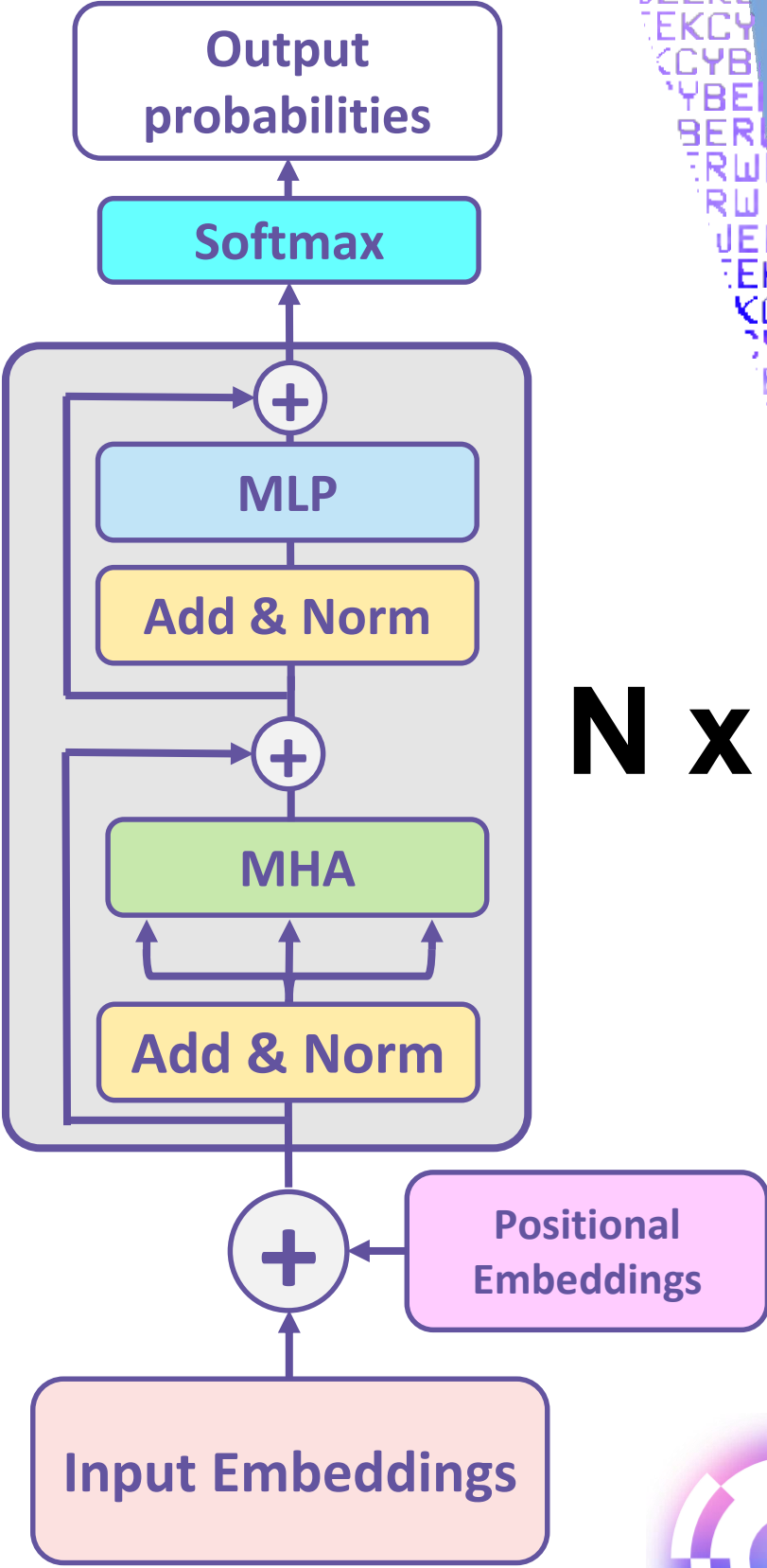


Critical SDC

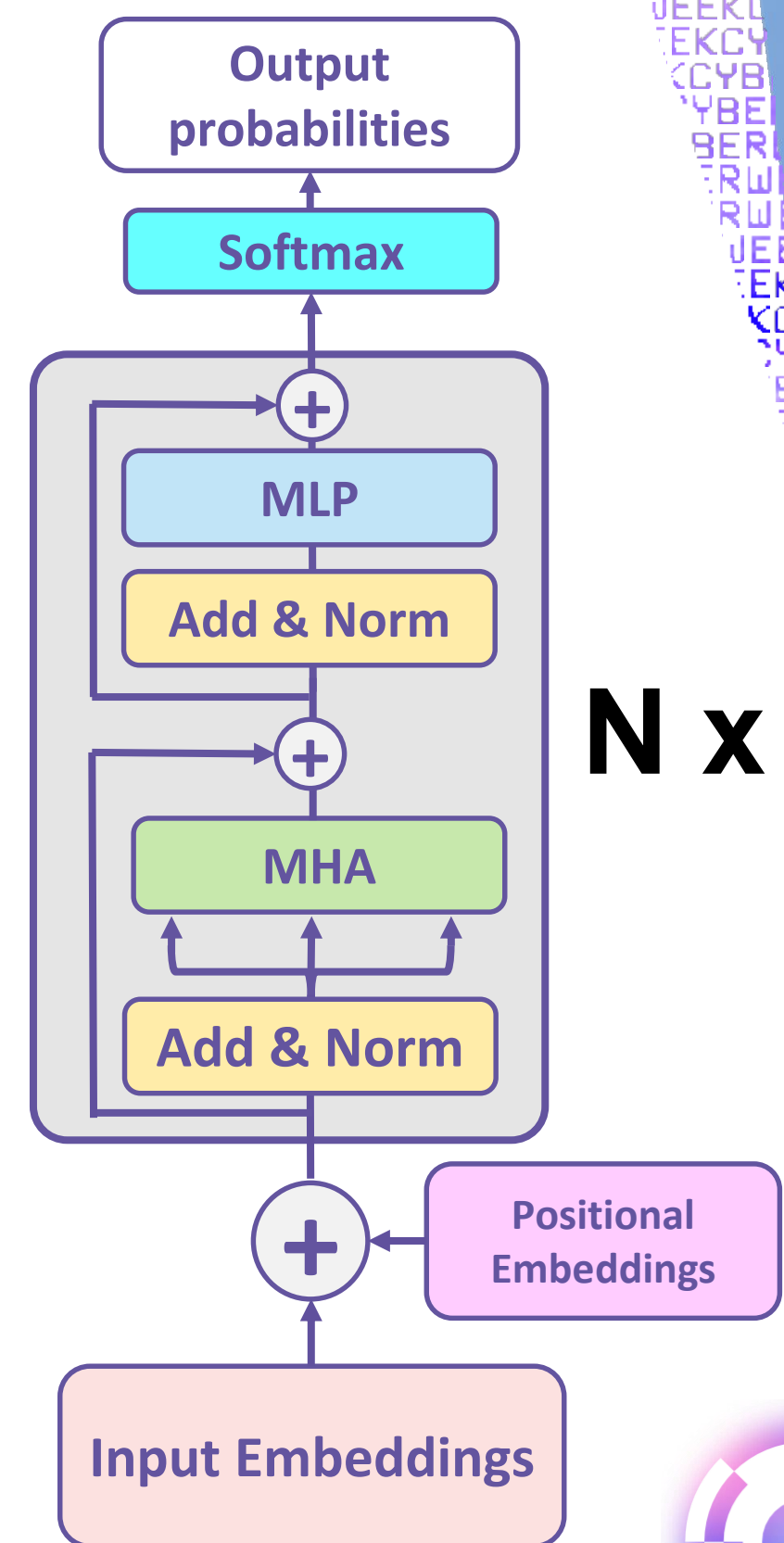
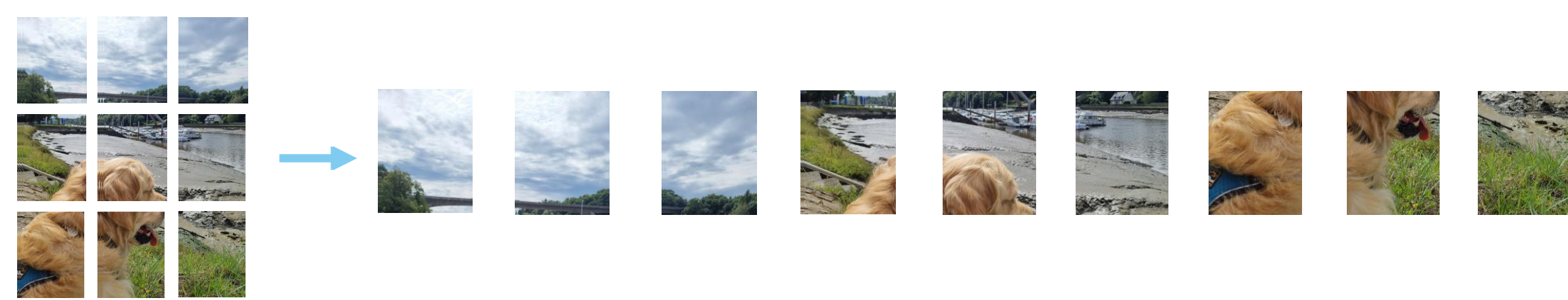


ViT Reliability Characterization

ViT Architecture

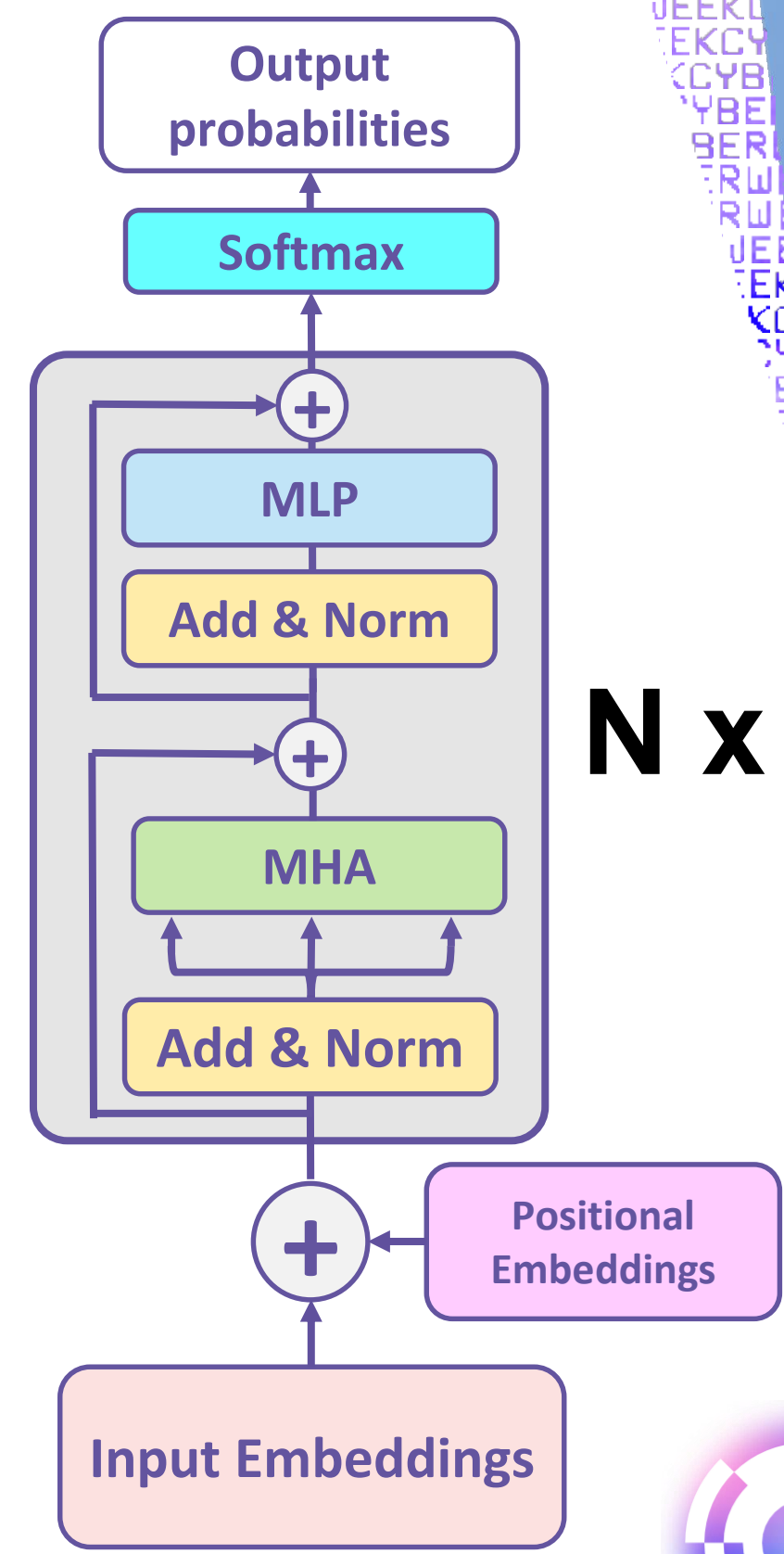


ViT Architecture

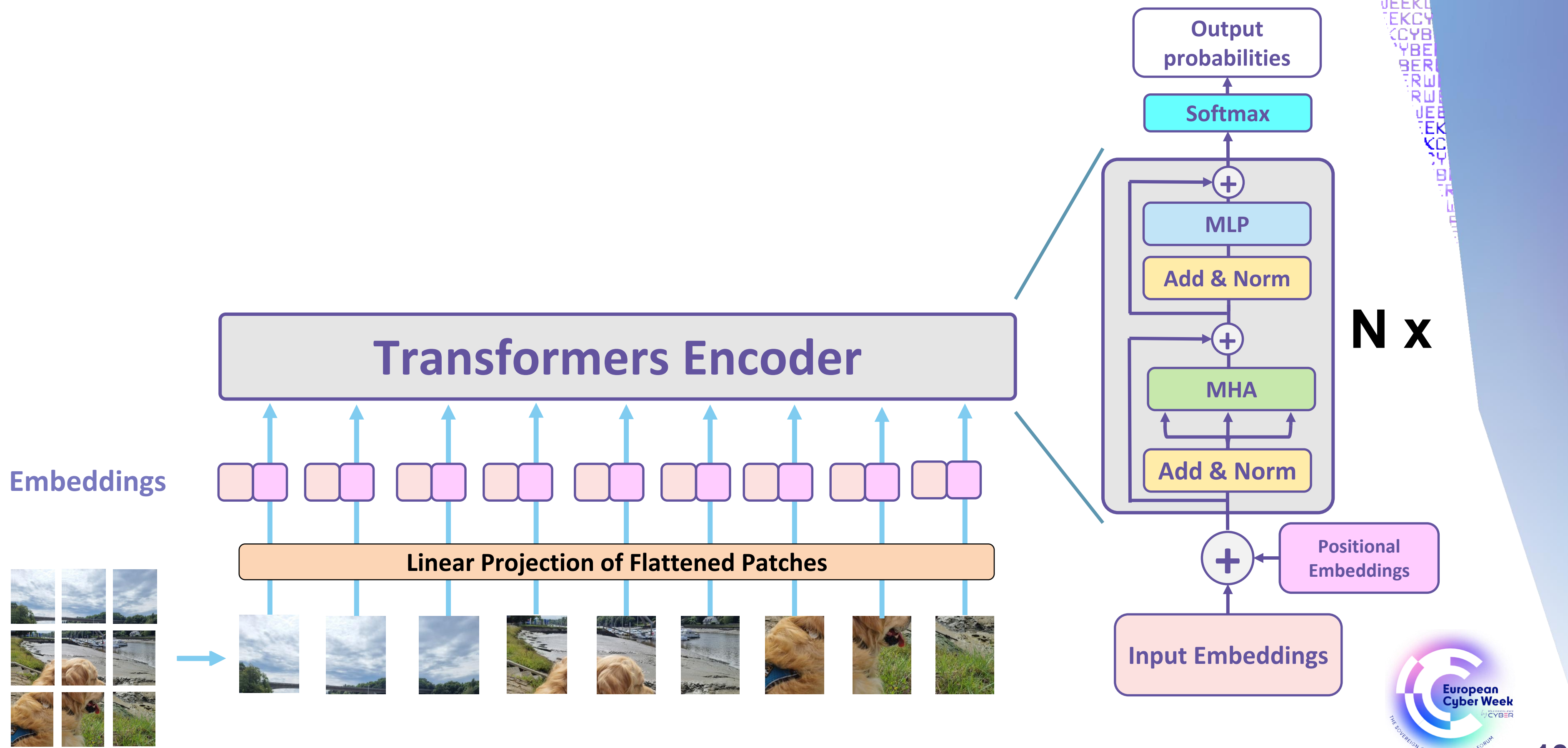


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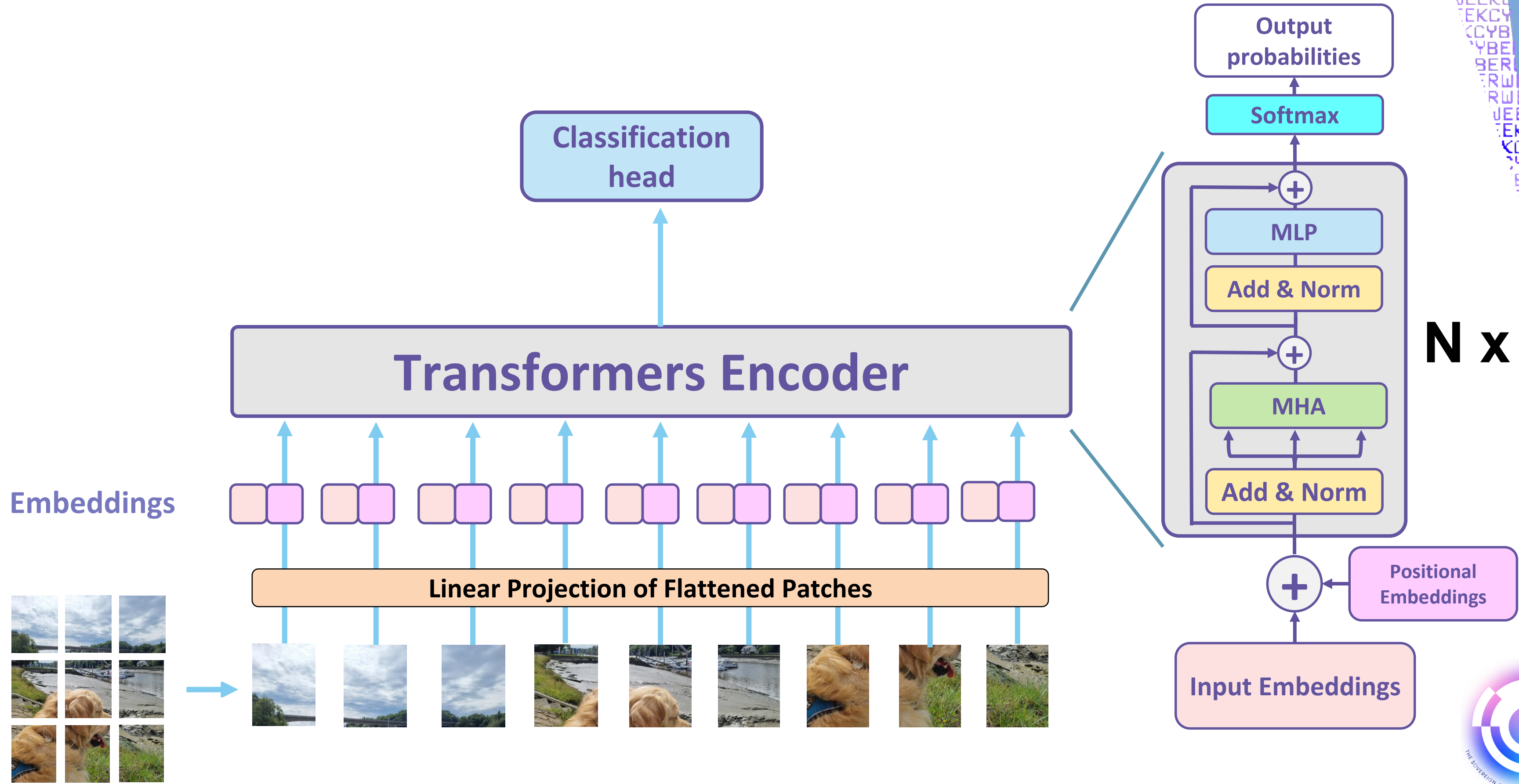
Embeddings



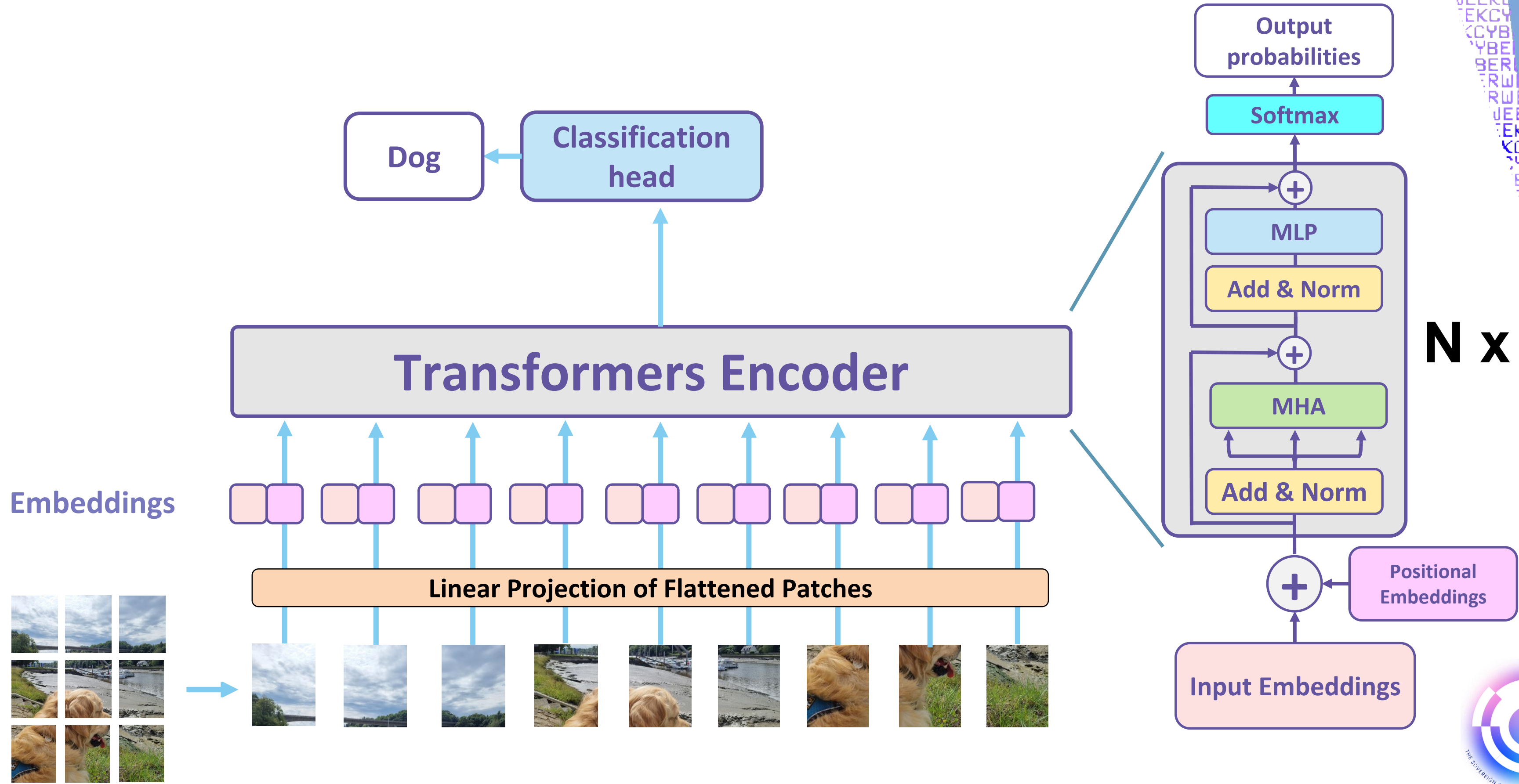
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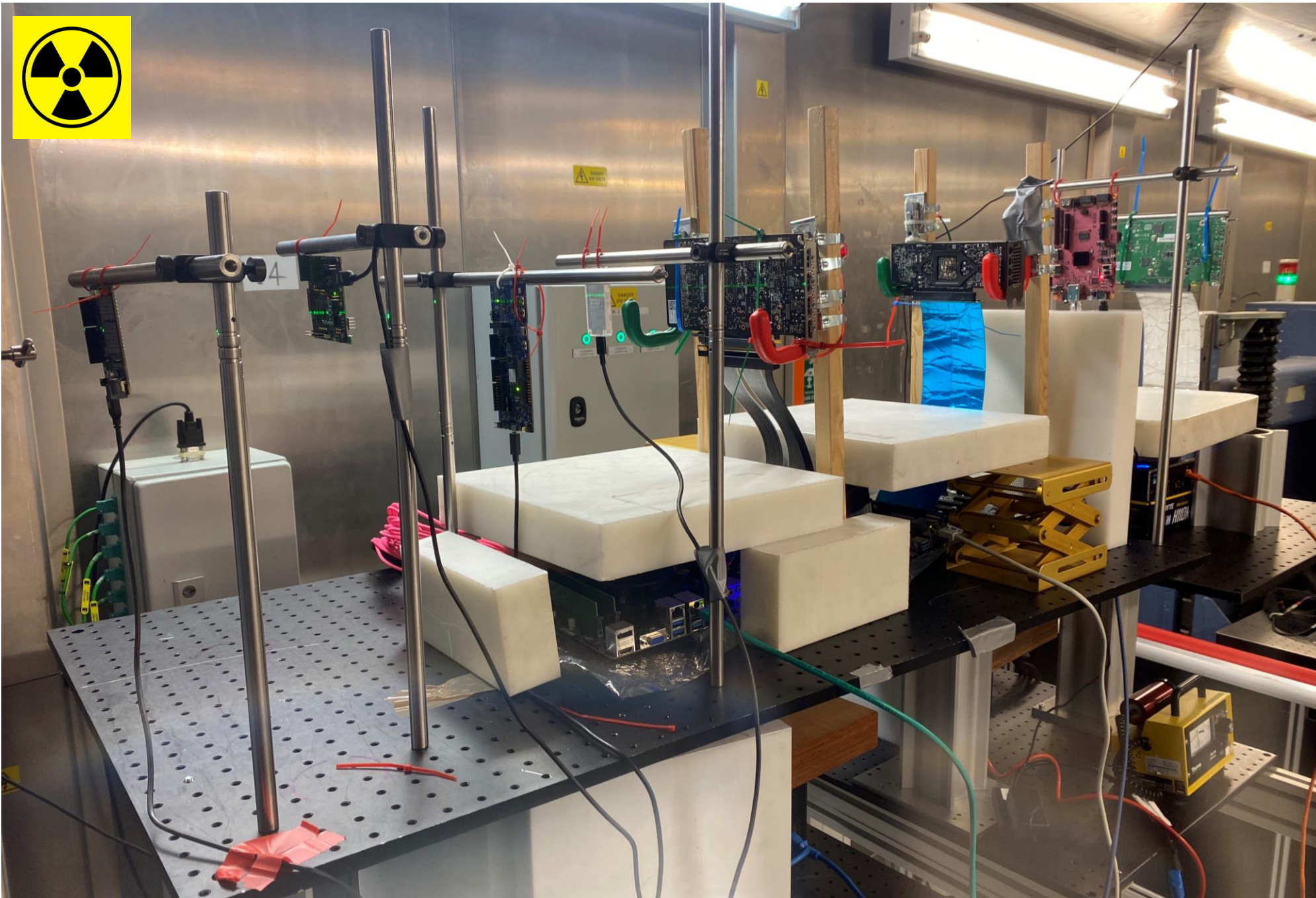
ViT Architecture



ViT Architecture

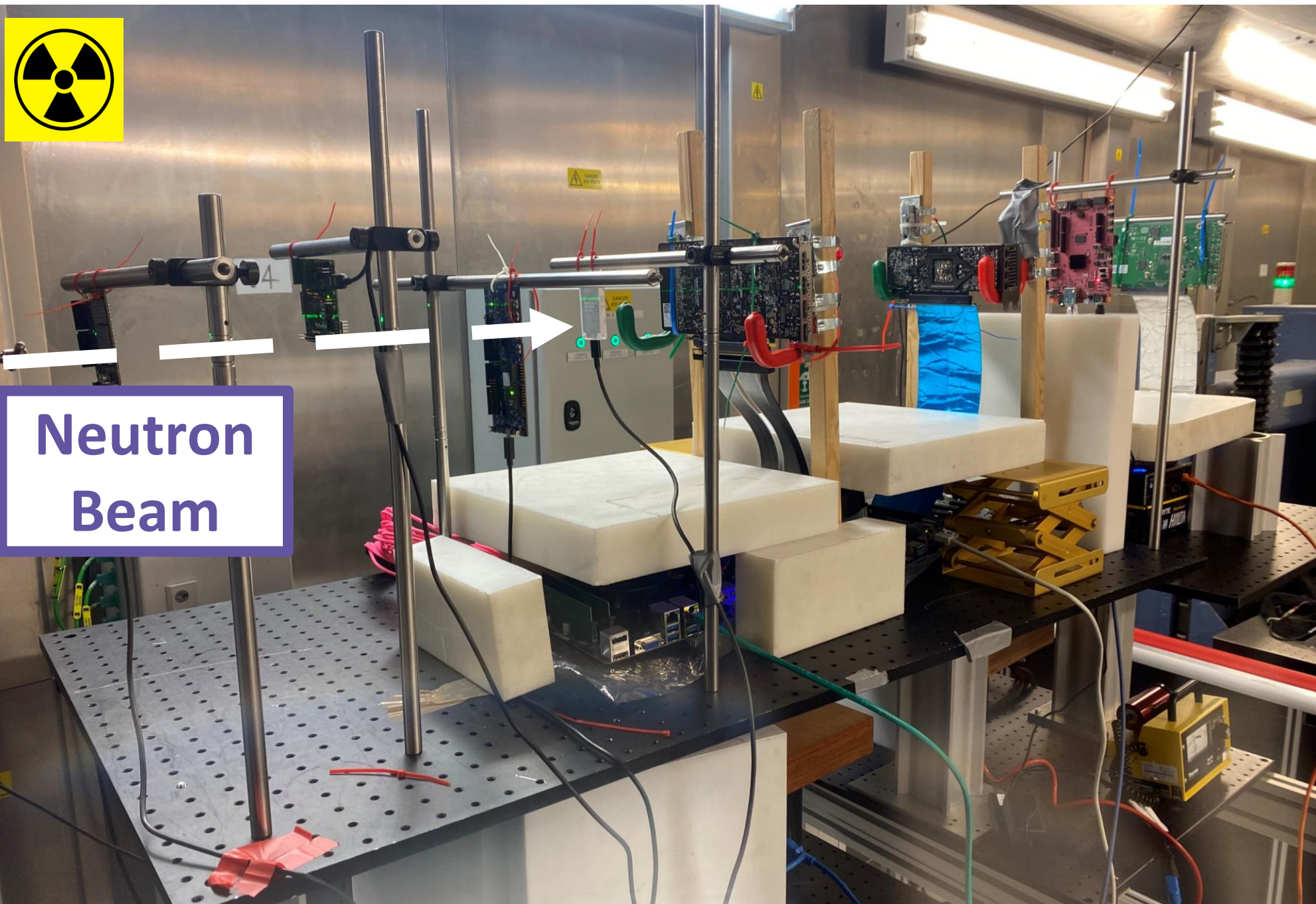


Neutron Beam Setup



Setup at ChipIR Facility, RAL, UK

Neutron Beam Setup

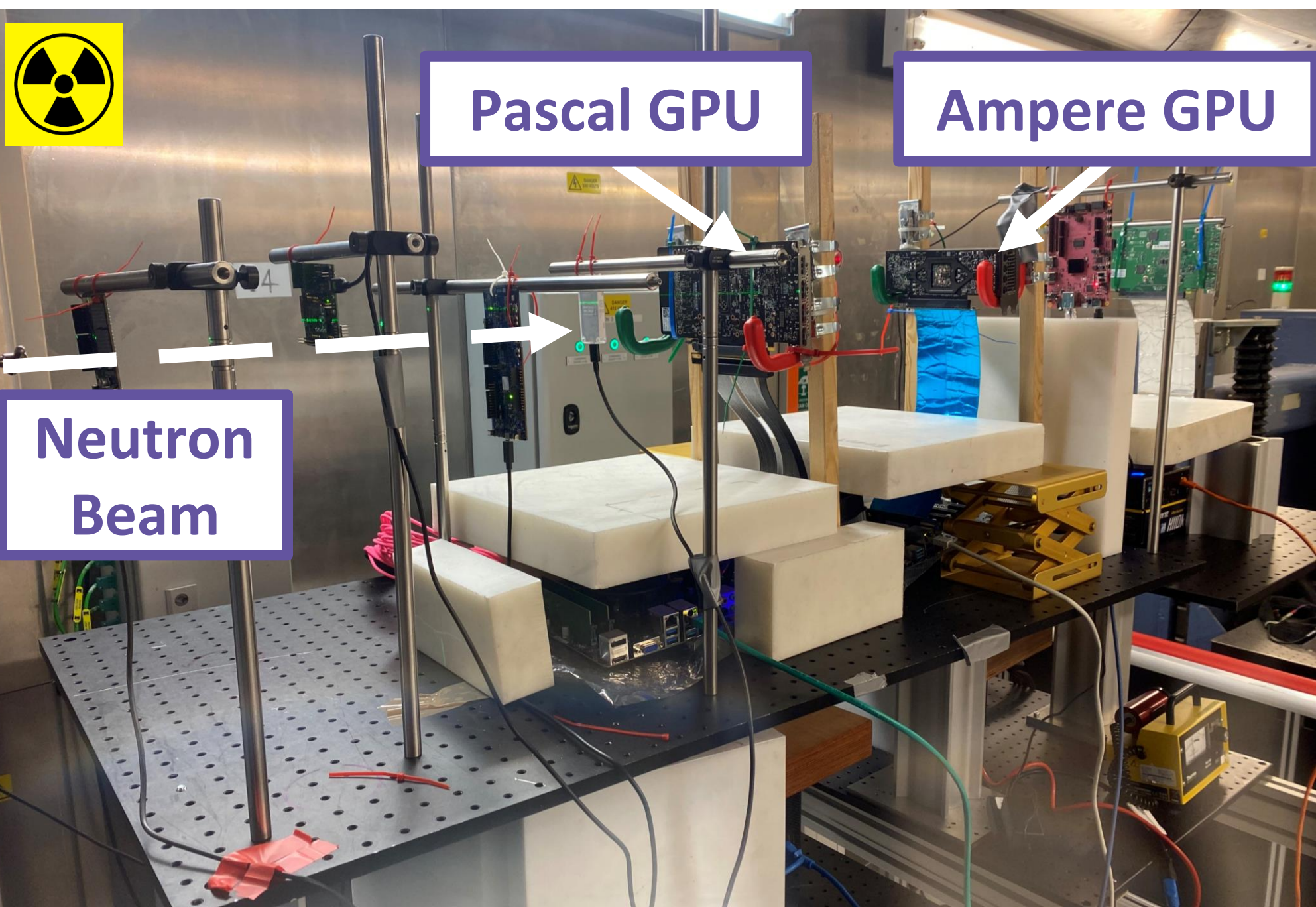


Neutron
Beam

Setup at ChipIR Facility, RAL, UK

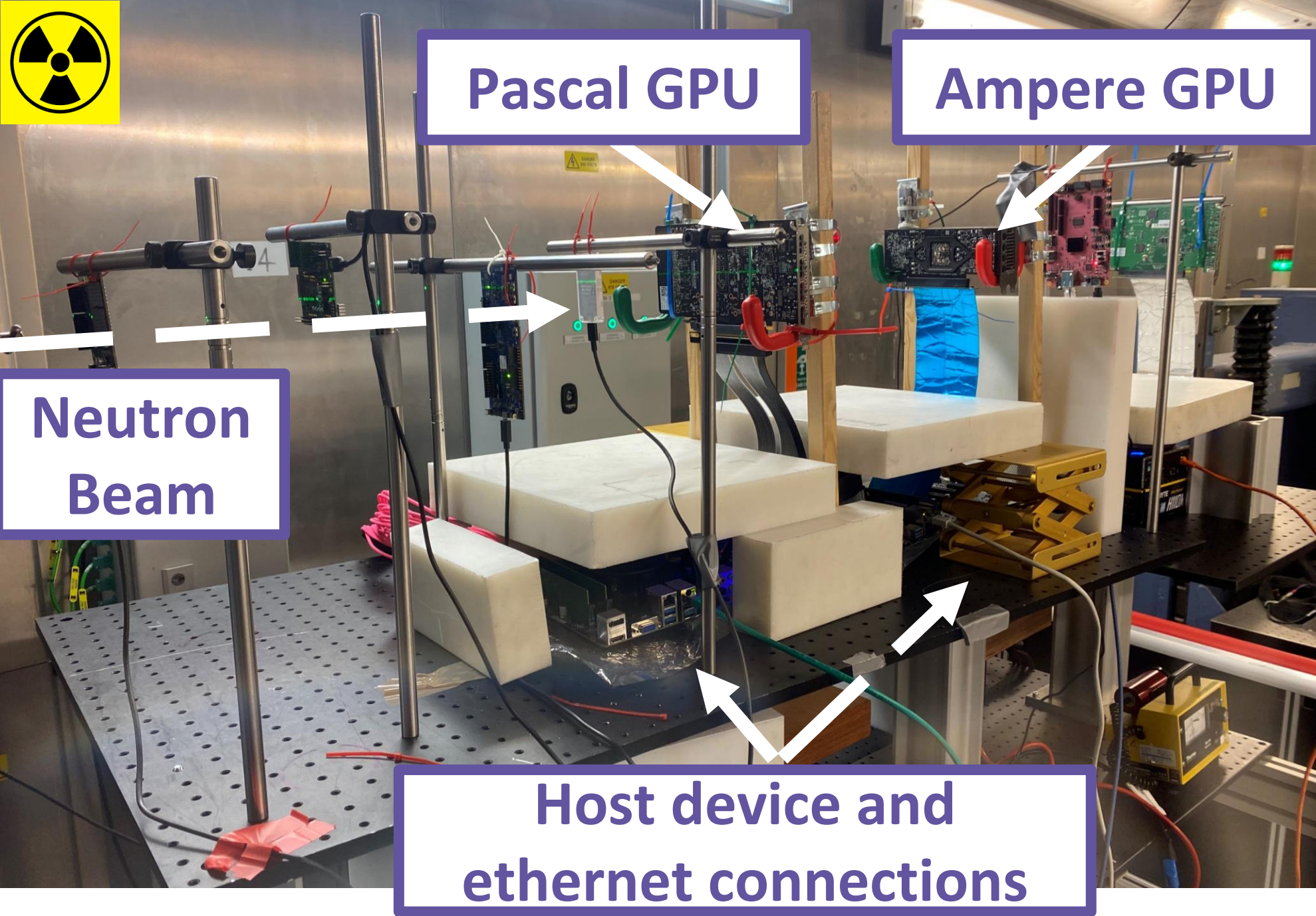


Neutron Beam Setup



Setup at ChipIR Facility, RAL, UK

Neutron Beam Setup



Setup at ChipIR Facility, RAL, UK

Neutron Beam Setup



Pascal GPU

Ampere GPU

Neutron
Beam

Host device and
ethernet connections

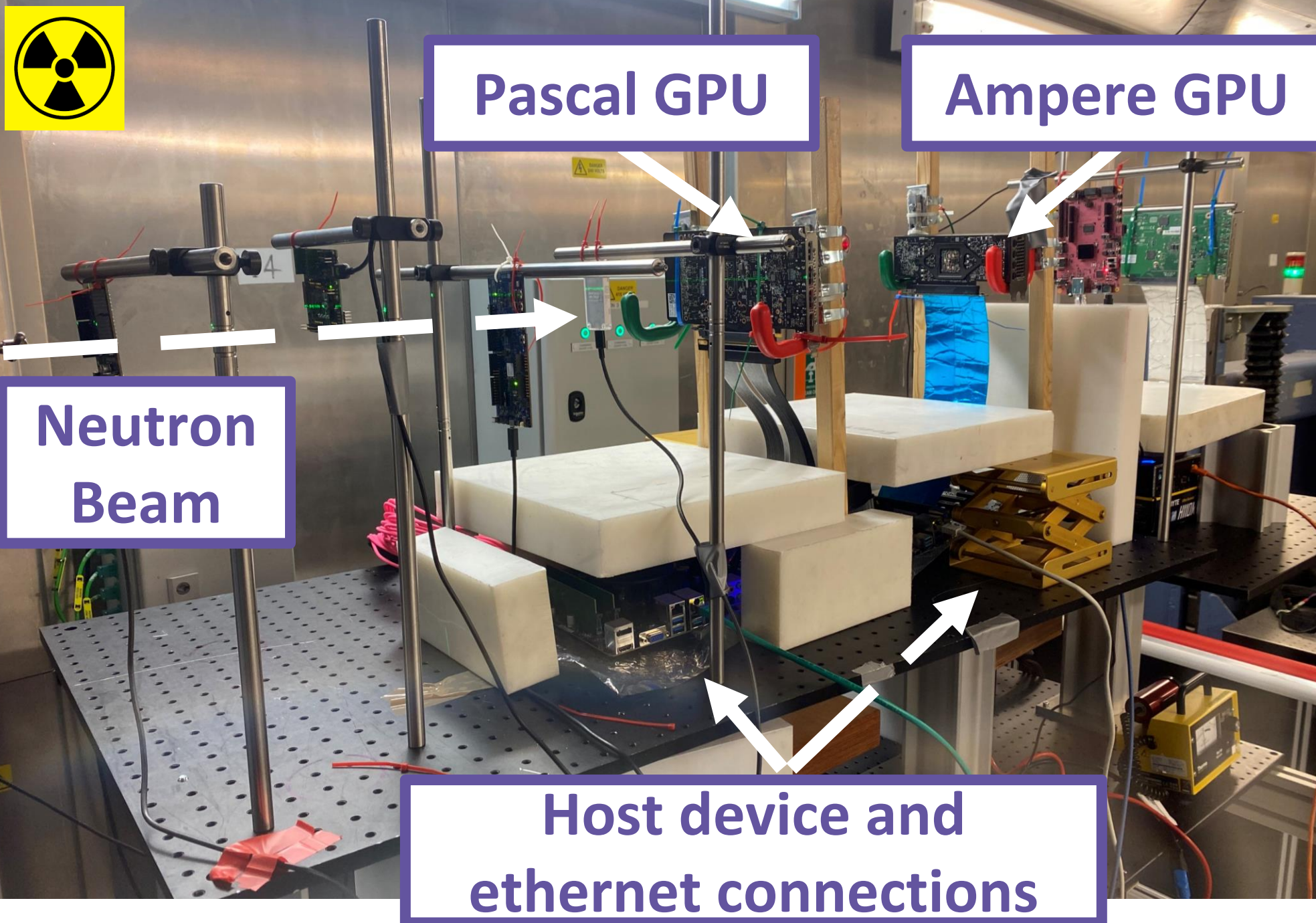
Failure Classification

- SDC: Tolerable and Critical
- DUE: ViT crashes, OS hangs

A server outside controls the experiments

Setup at ChipIR Facility, RAL, UK

Neutron Beam Setup



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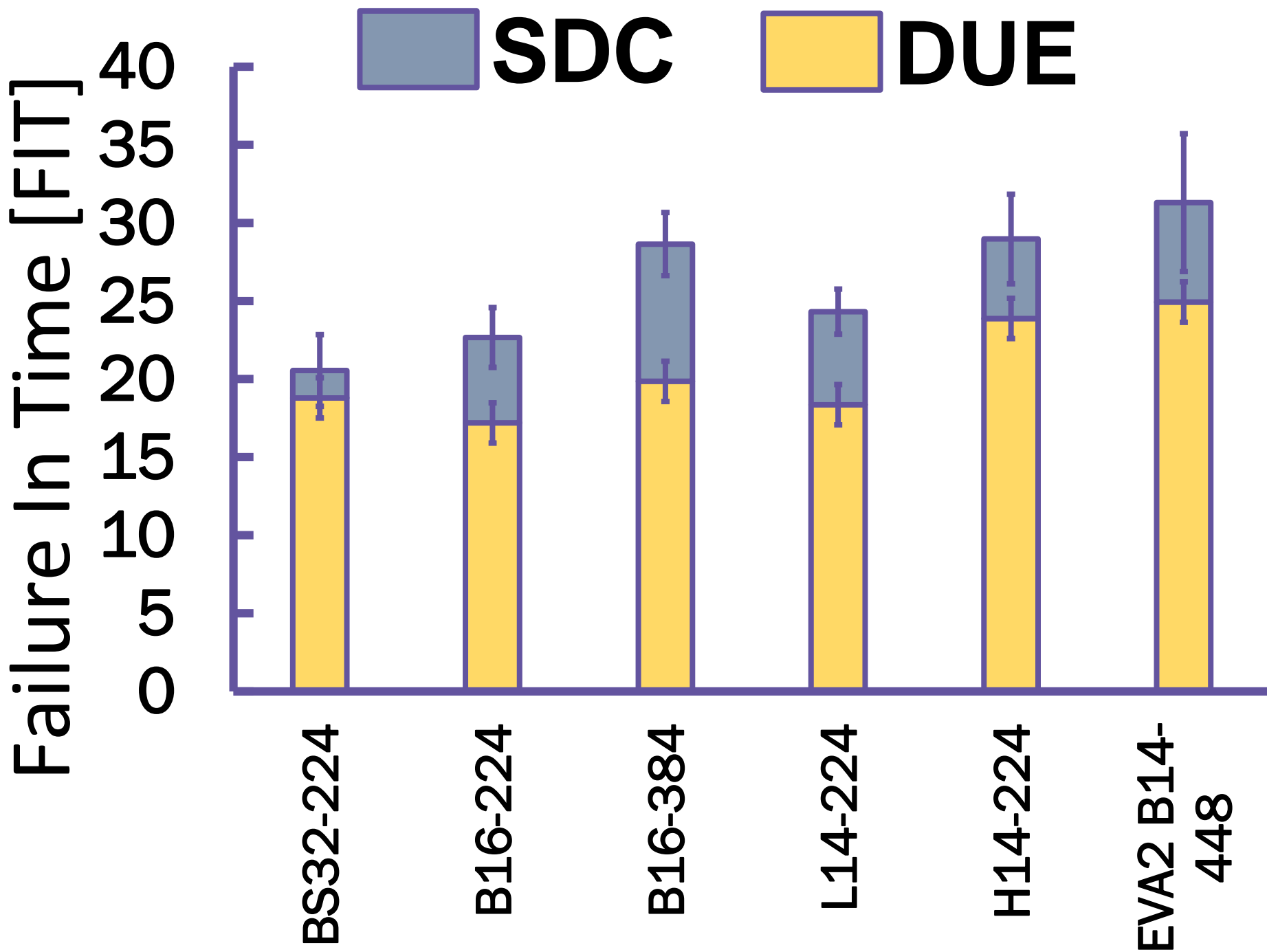
A server outside controls the experiments

Configurations

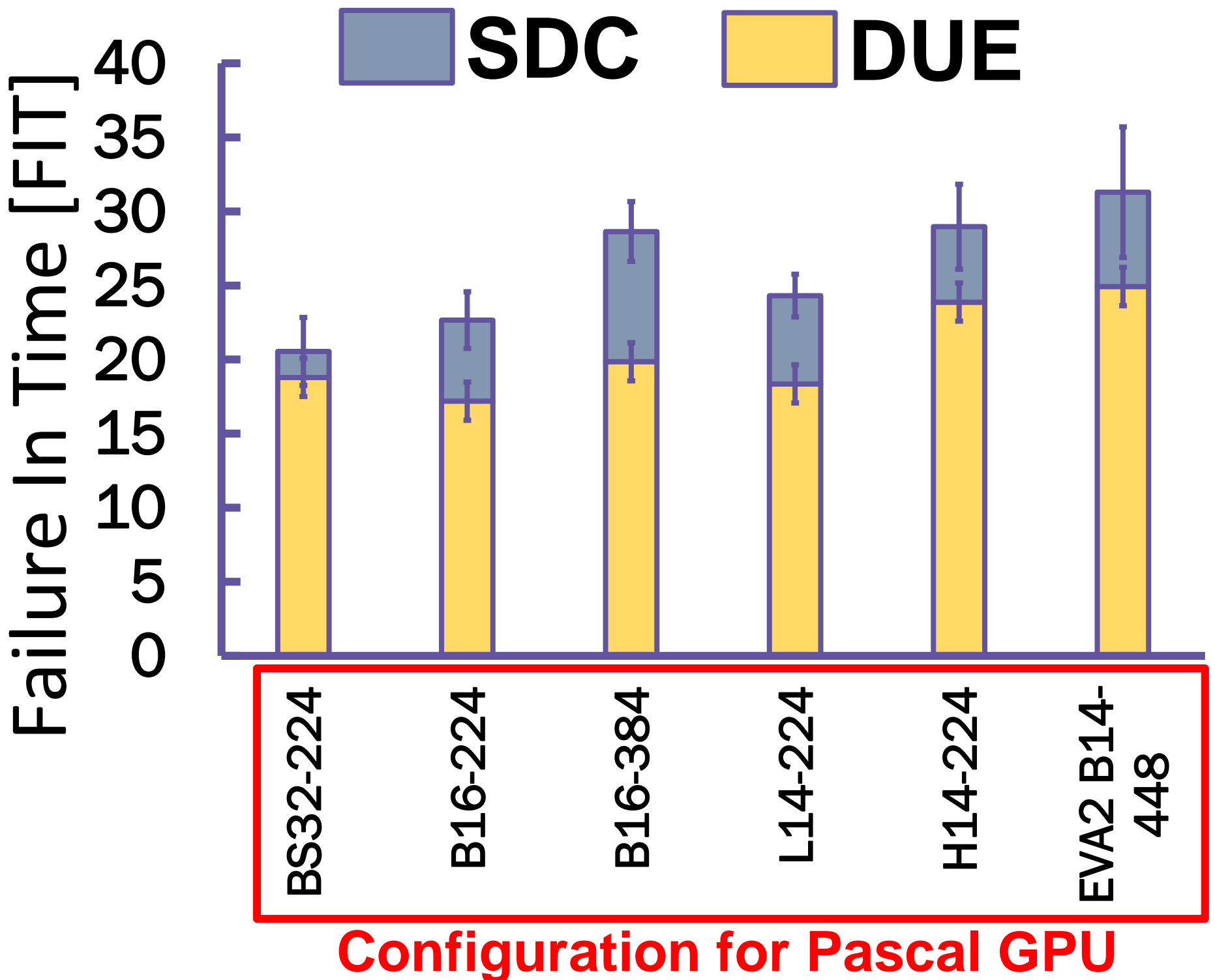
- Dataset: ImageNet (classification)
- 12 ViTs, 4 families: ViT, EVA2, SwinV2 and MaxViT
- 2 NVIDIA GPUs: Quadro P2000 (Pascal) and RTX A2000 (Ampere)

Setup at ChipIR Facility, RAL, UK

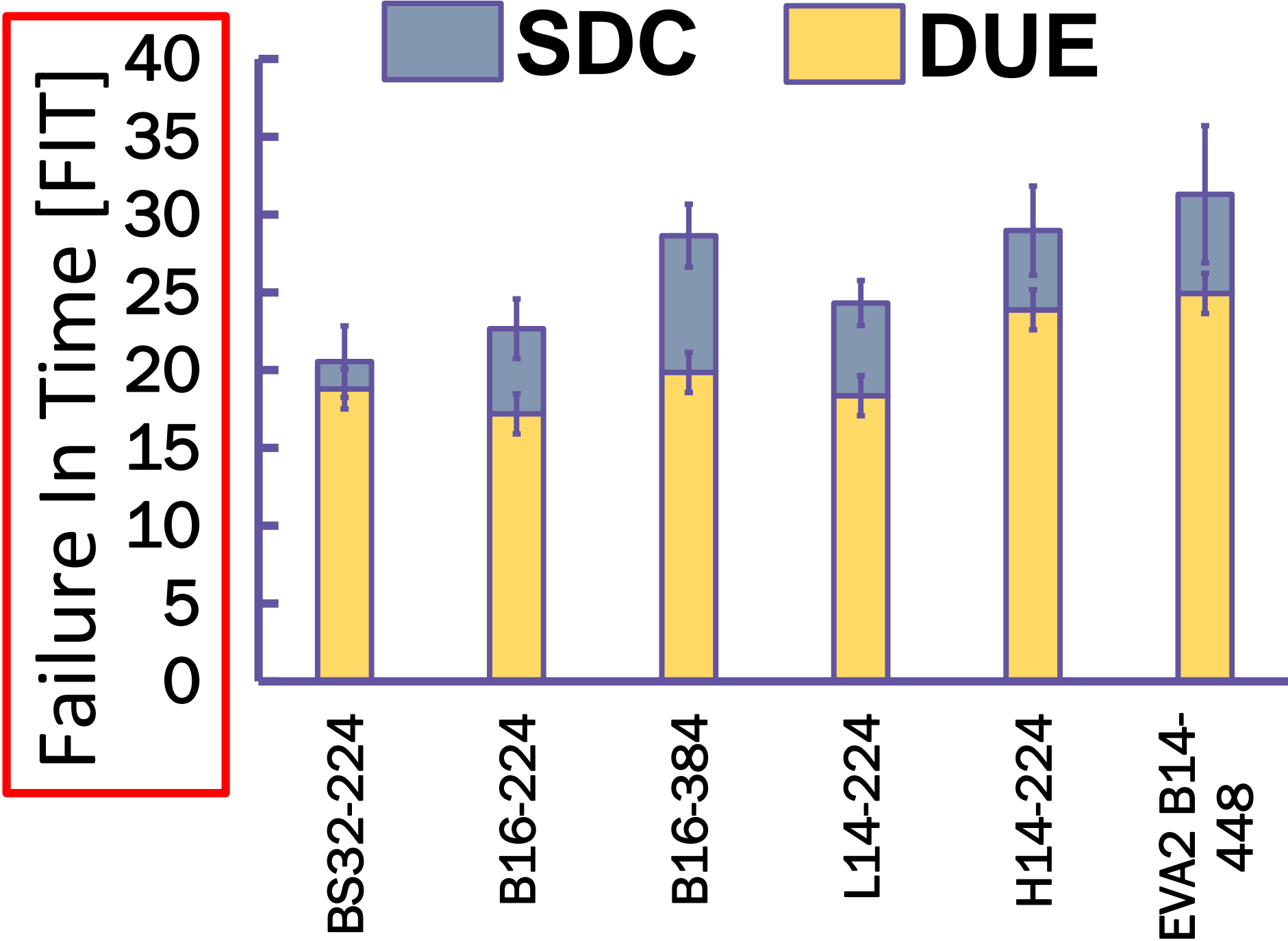
ViT's Failure in Time (FIT)



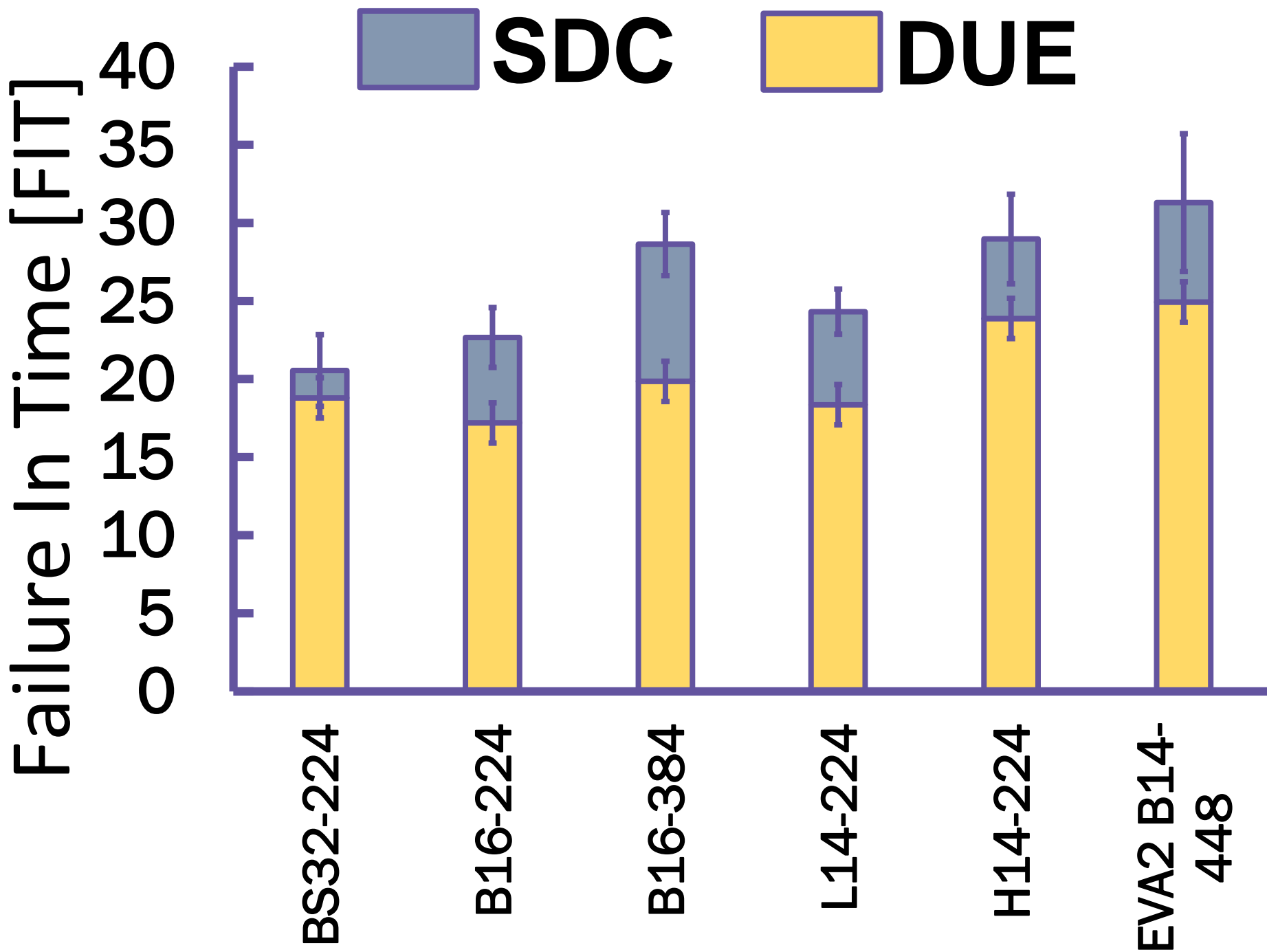
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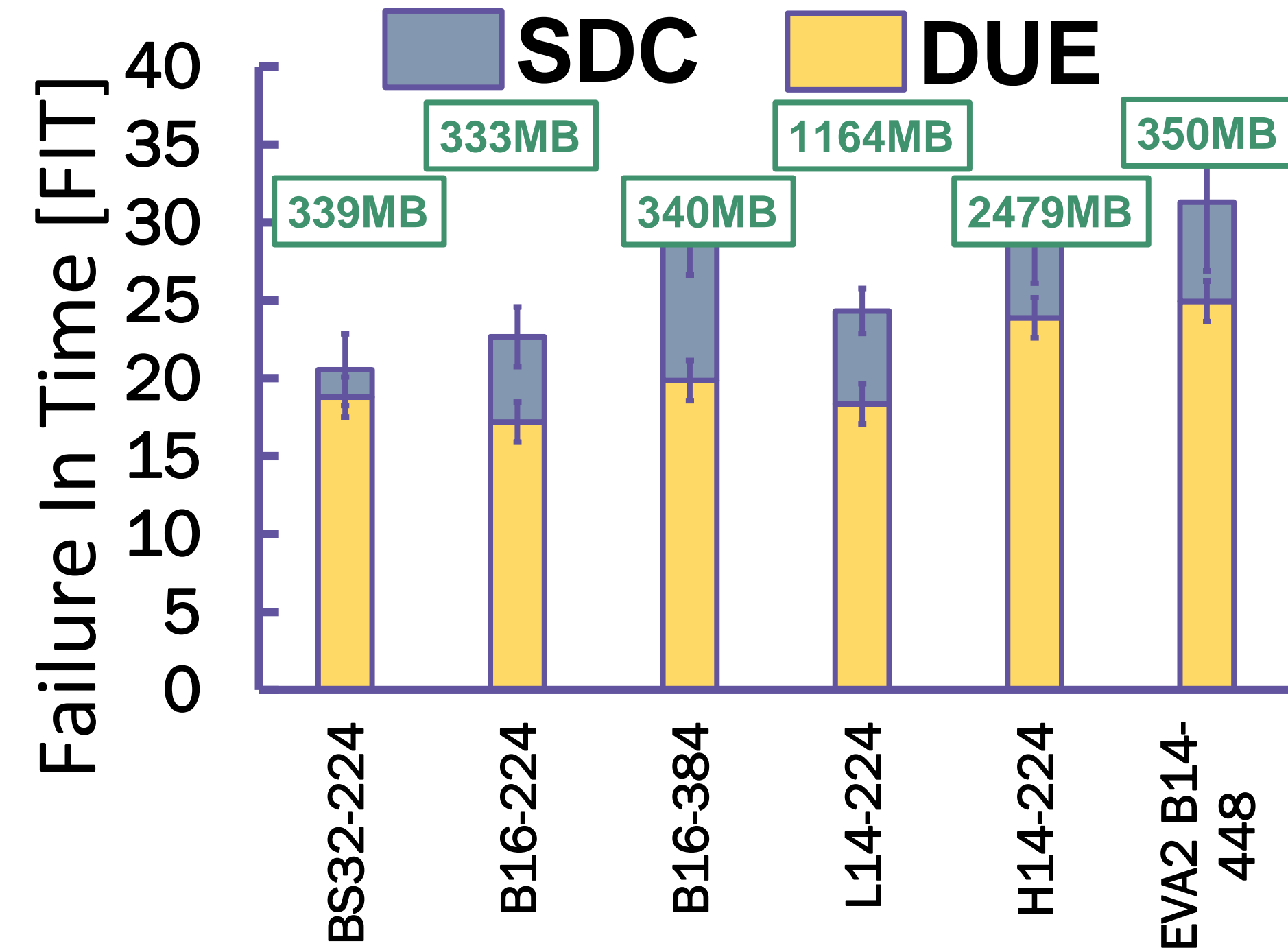
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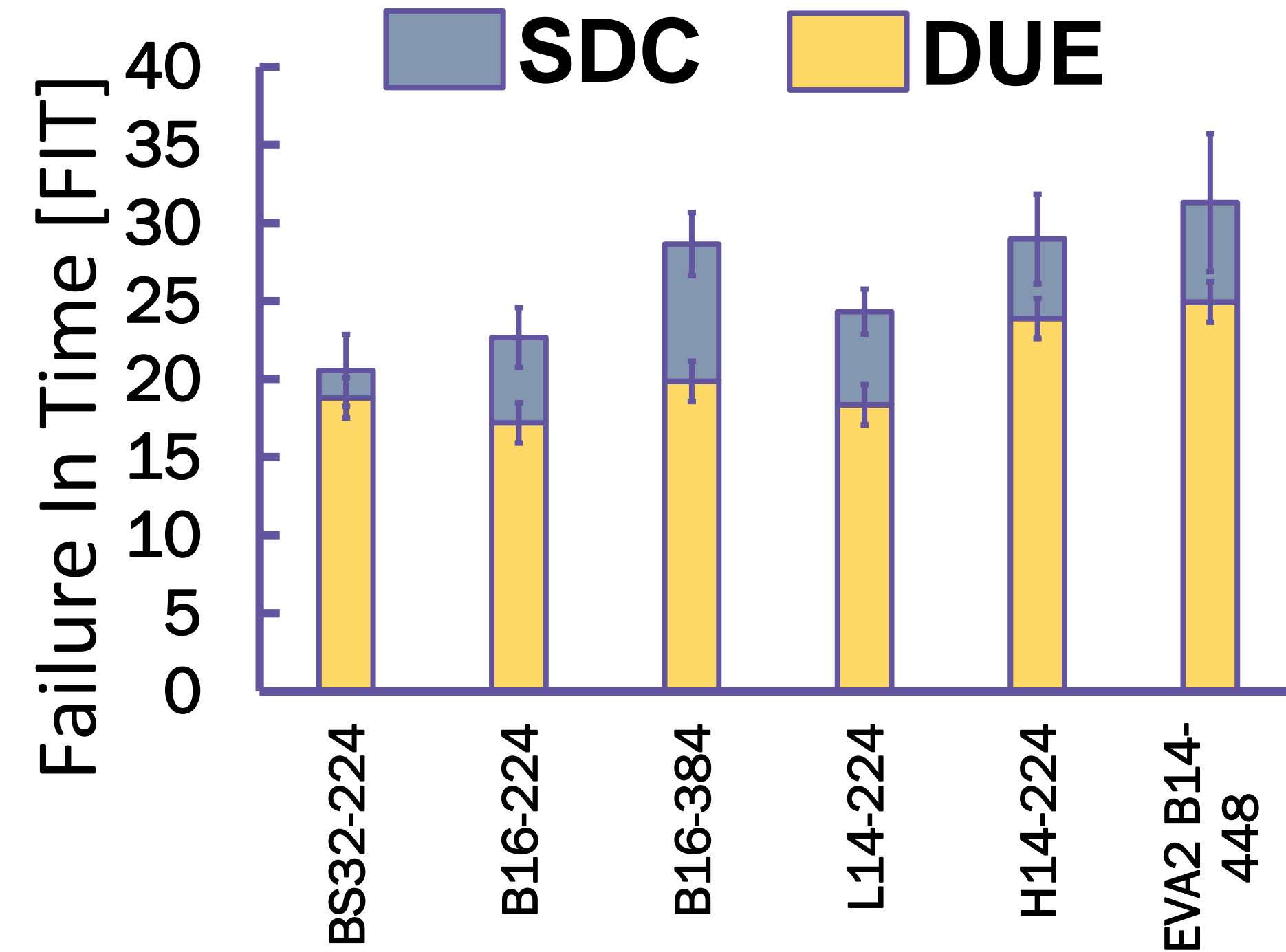


ViT's Failure in Time (FIT)



- The ViT FIT grows along its resource usage and family

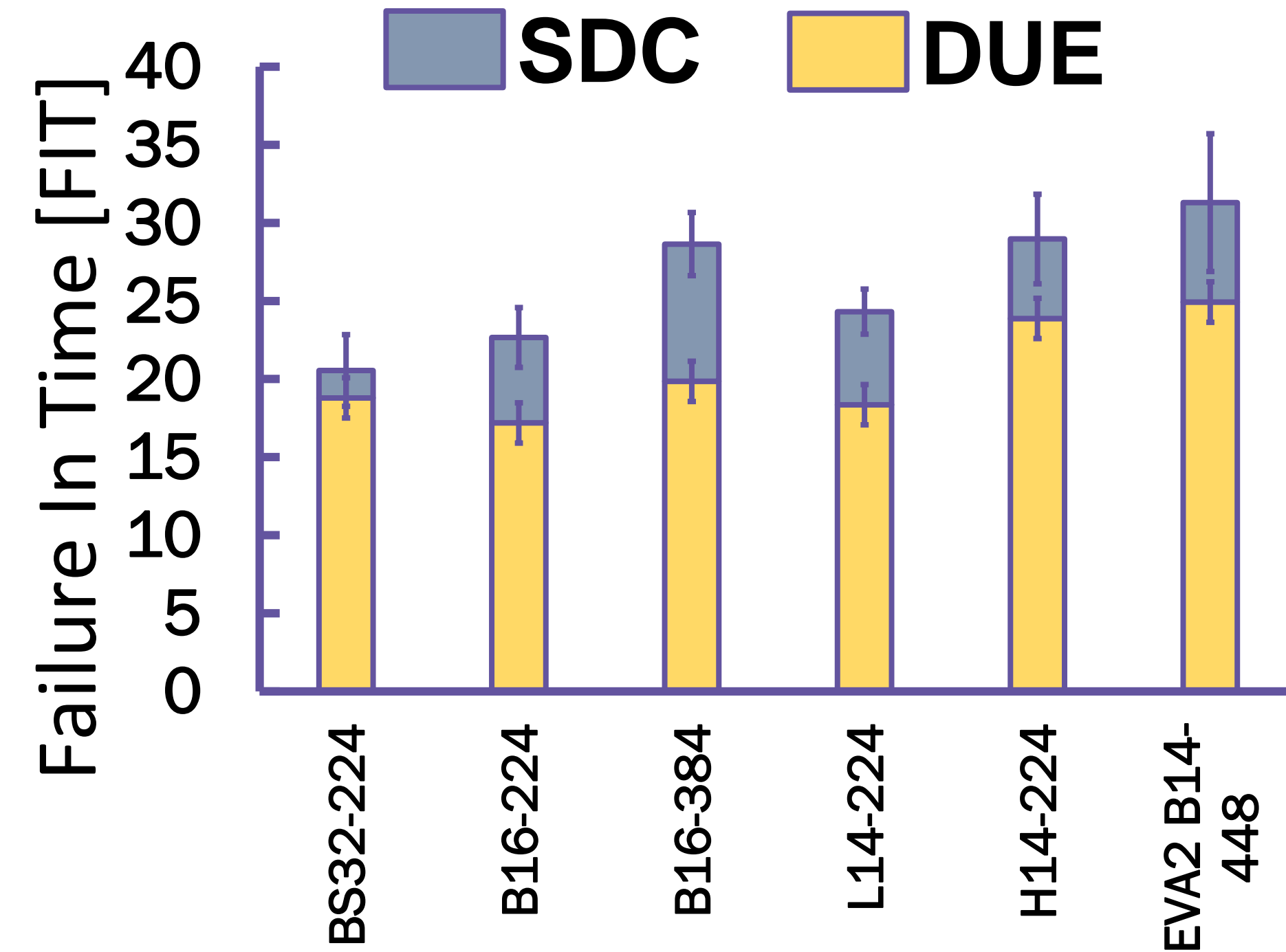
ViT's Failure in Time (FIT)



- On average, 14.7% of Critical SDCs (max. 33.3%)

- The ViT FIT grows along its resource usage and family

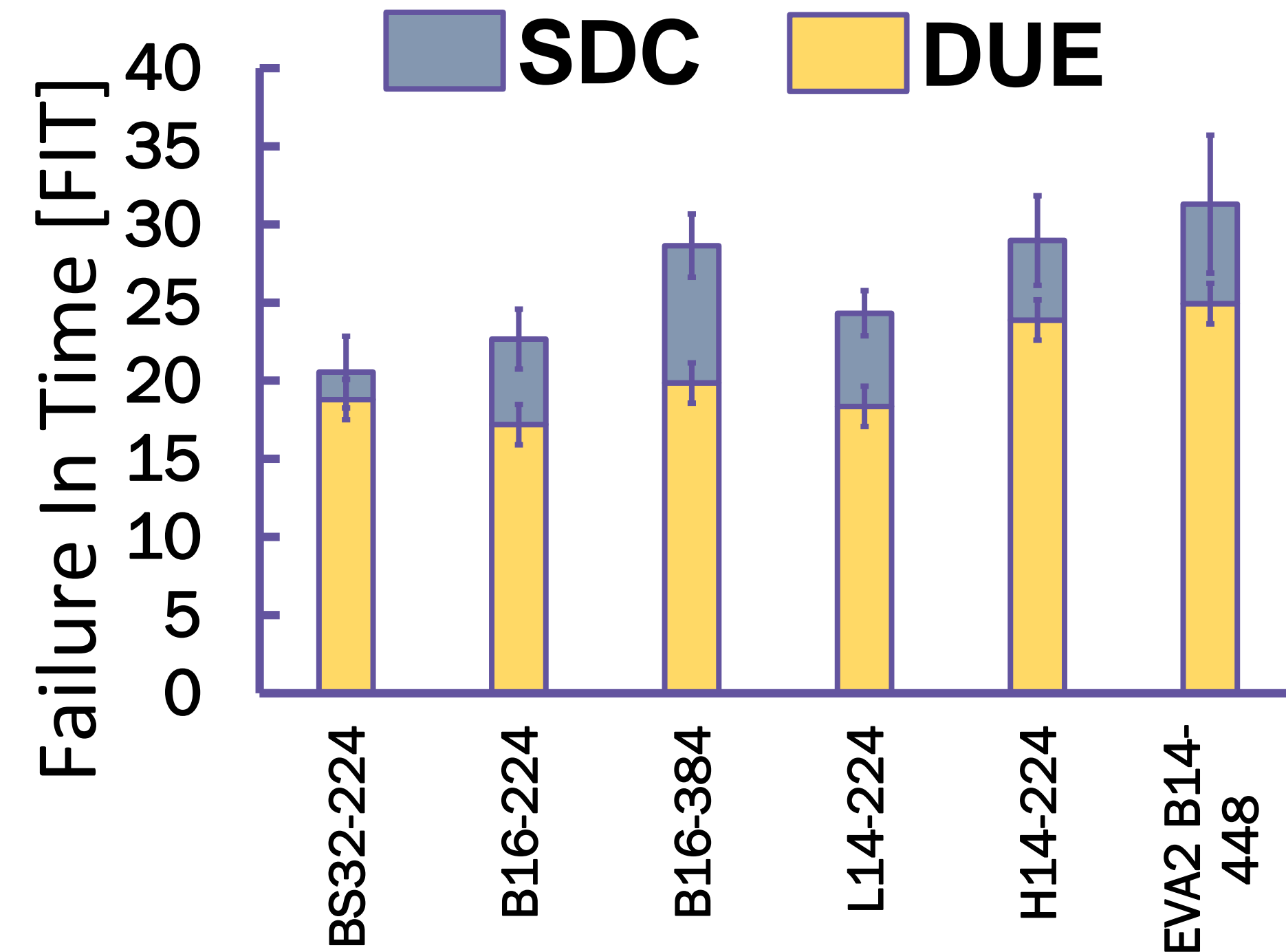
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- Same observations for Ampere GPU

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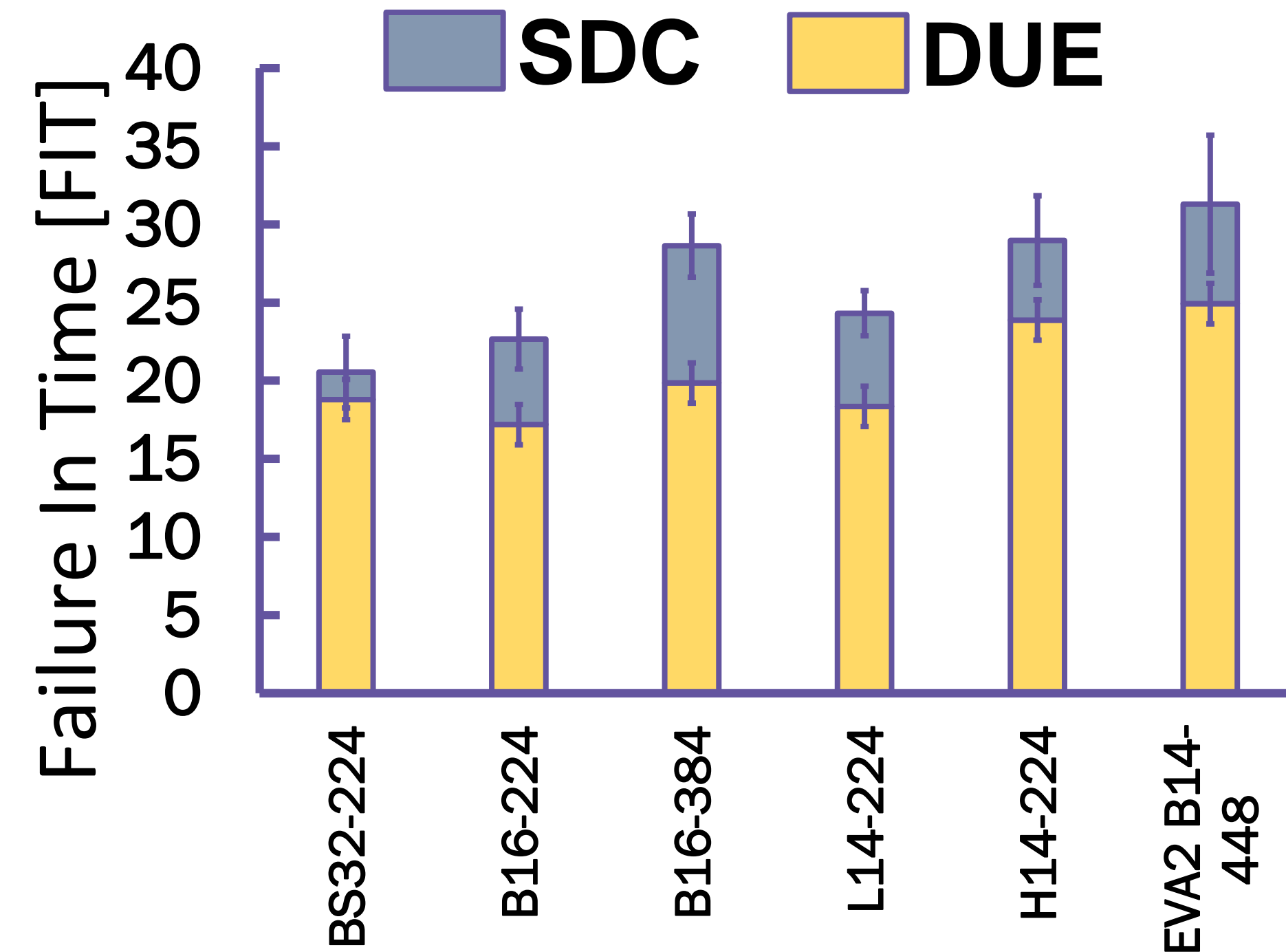
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ViT's Failure in Time (FIT)

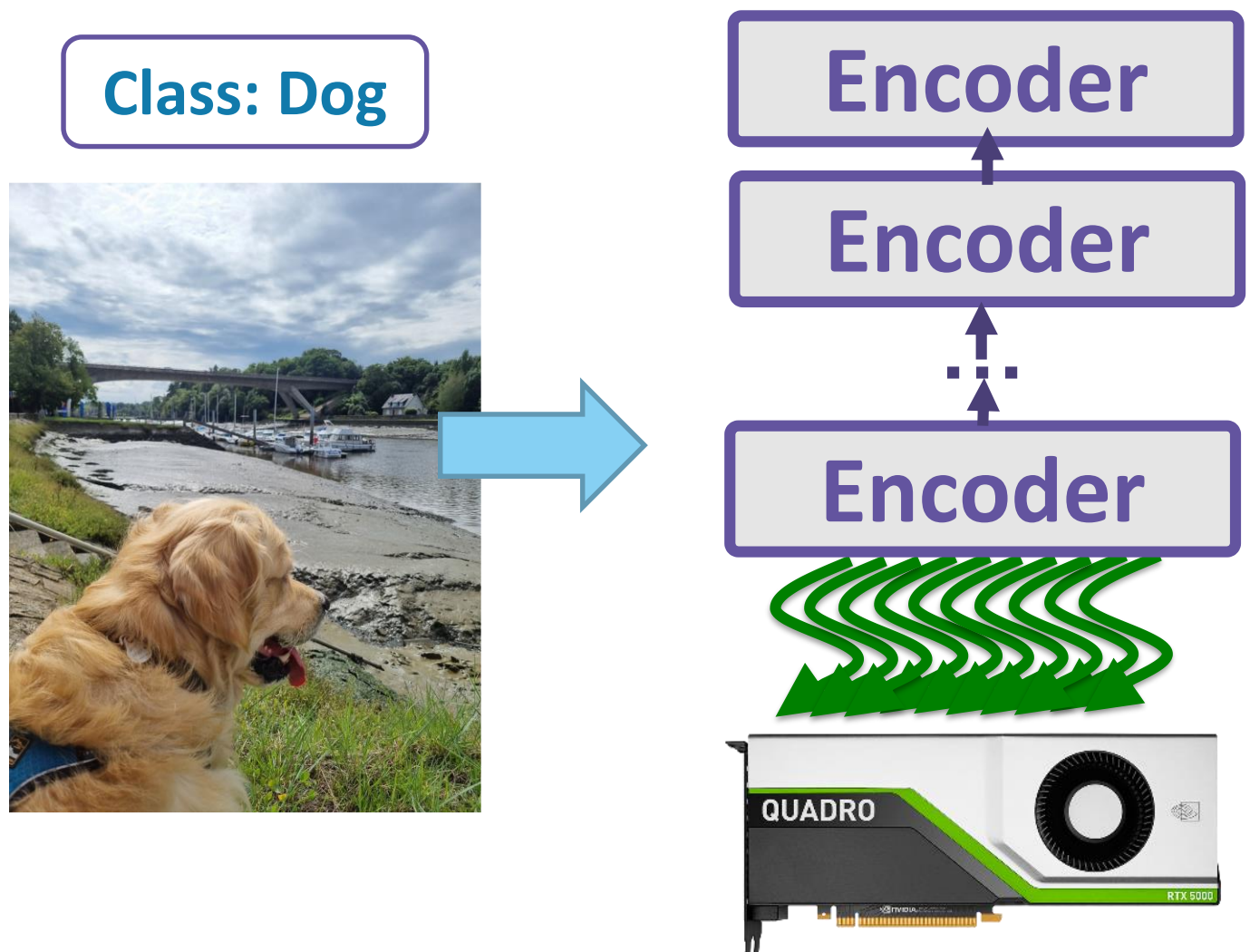


- On average, 14.7% of Critical SDCs (max. 33.3%)
- Same observations for Ampere GPU
 - 16.3% of Critical SDCs on average (max. 37.3%)
 - with ECC ON: up to 10.0% of Critical SDCs

- The ViT FIT grows along its resource usage and family

Efficient Fault Tolerance

Fault Impact on ViTs

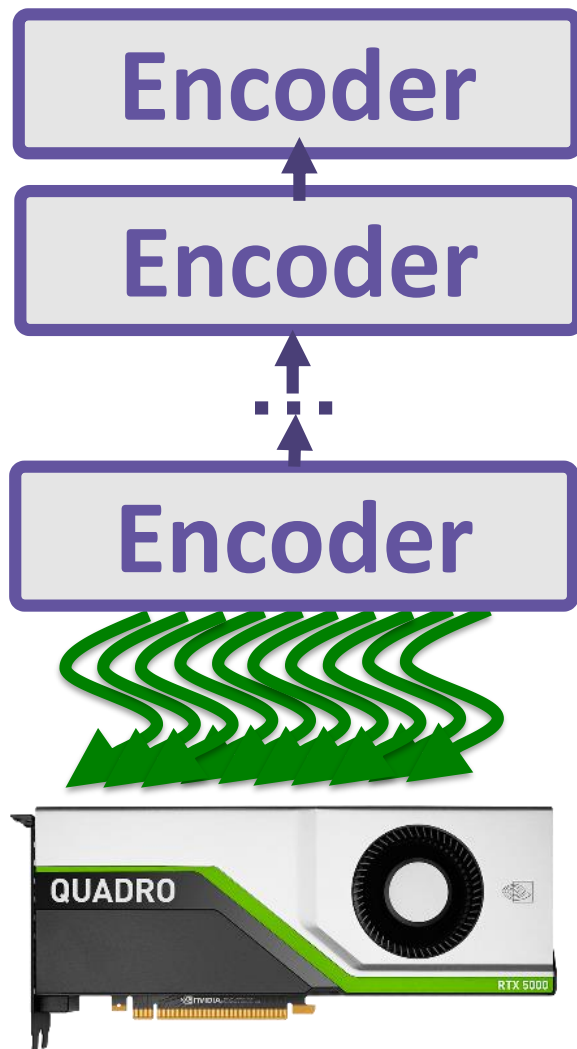
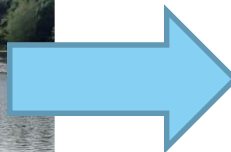
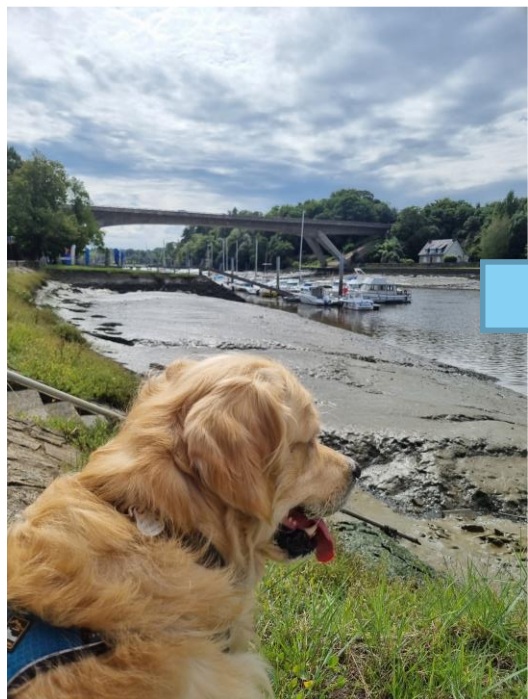


Fault Impact on ViTs

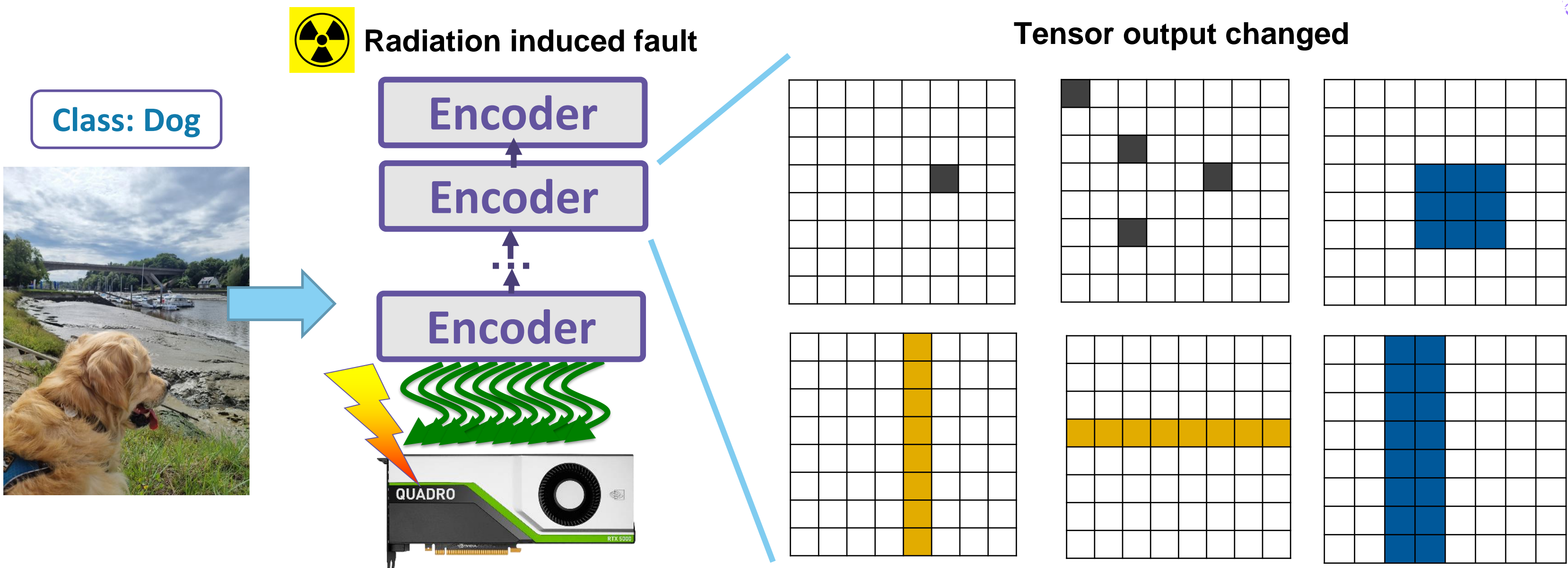


Radiation induced fault

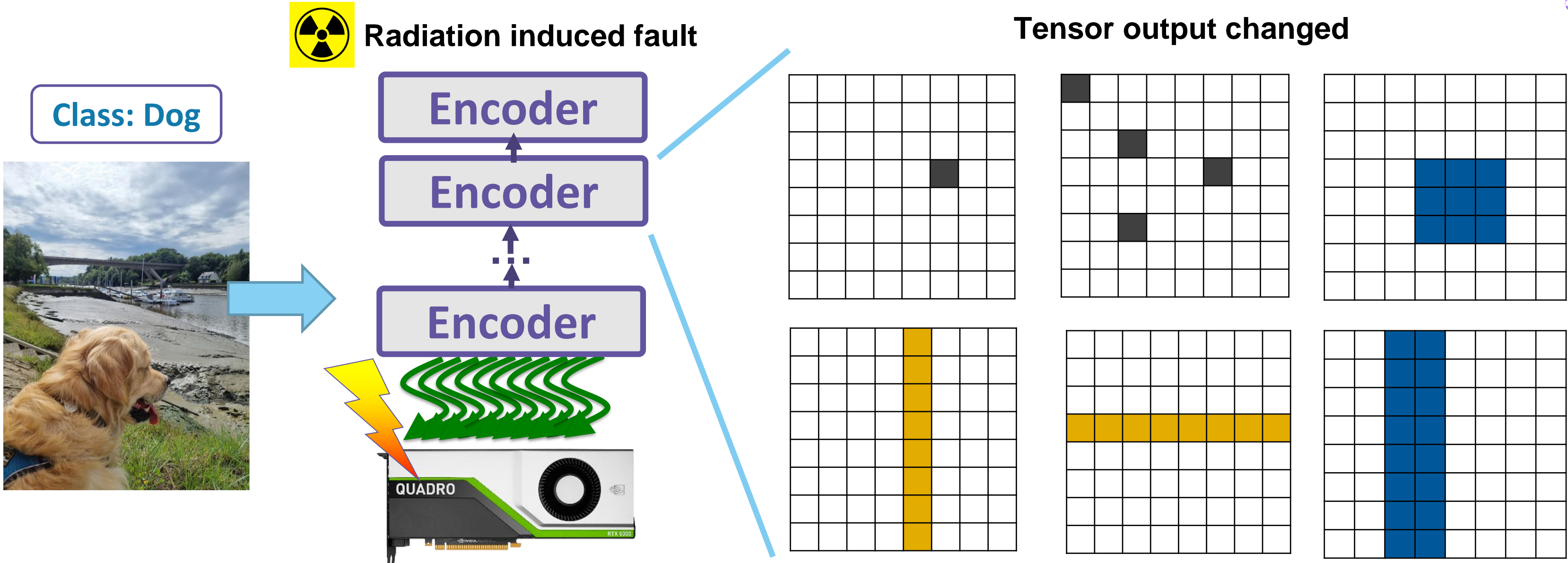
Class: Dog



Fault Impact on ViTs



Fault Impact on ViTs



Fault assessement

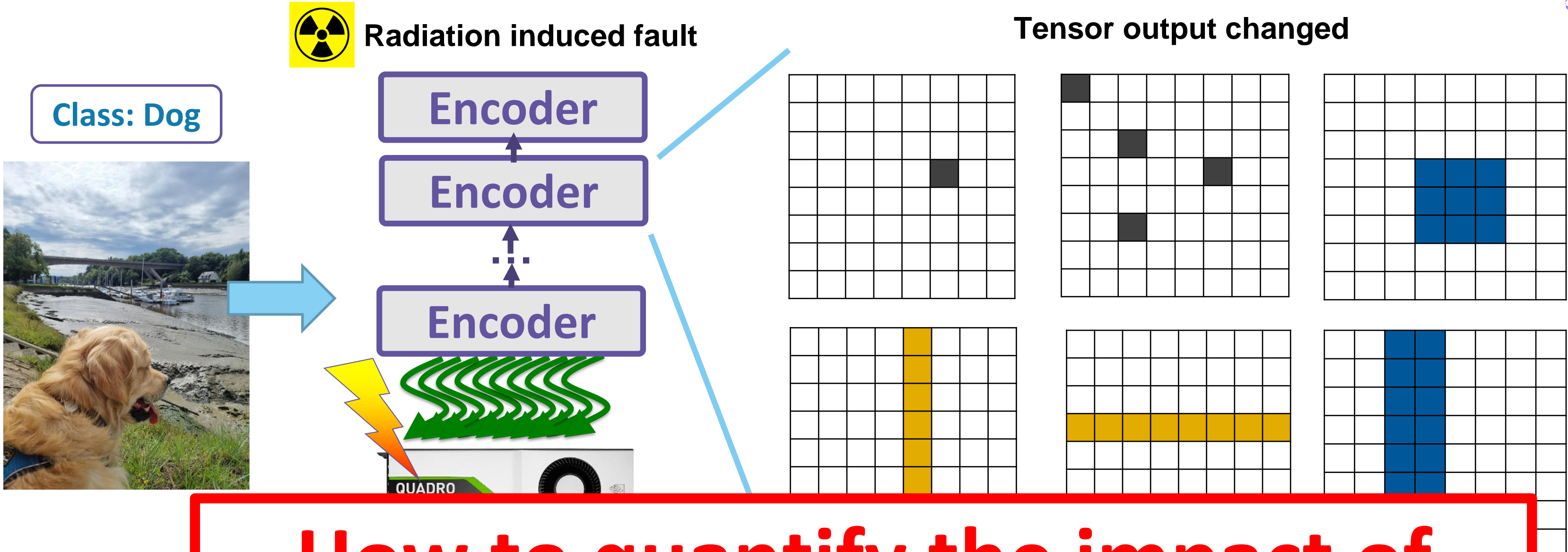
- Physical fault injection
- Software fault simulation



```
int foo(S,P){  
  int r;  
  if (S==P)  
    r=1;  
  else  
    r=0;  
  return r;  
}
```



Fault Impact on ViTs



- Fault**
- Phys
 - Software fault simulation

How to quantify the impact of faults ?

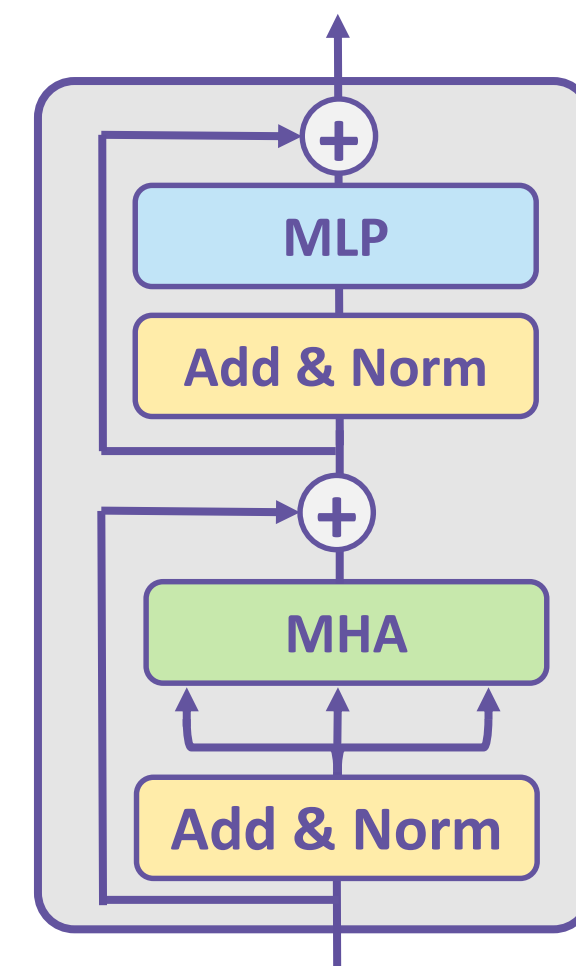


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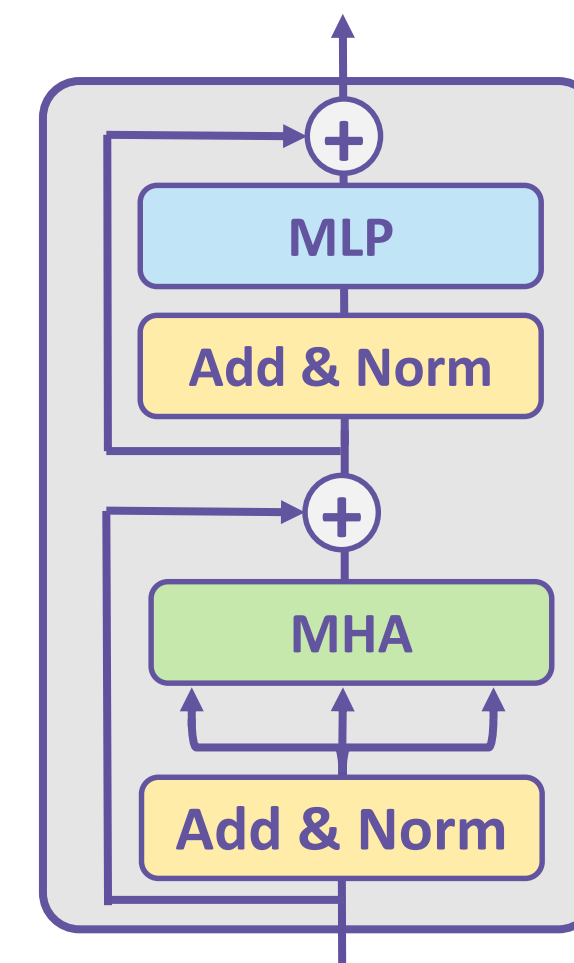
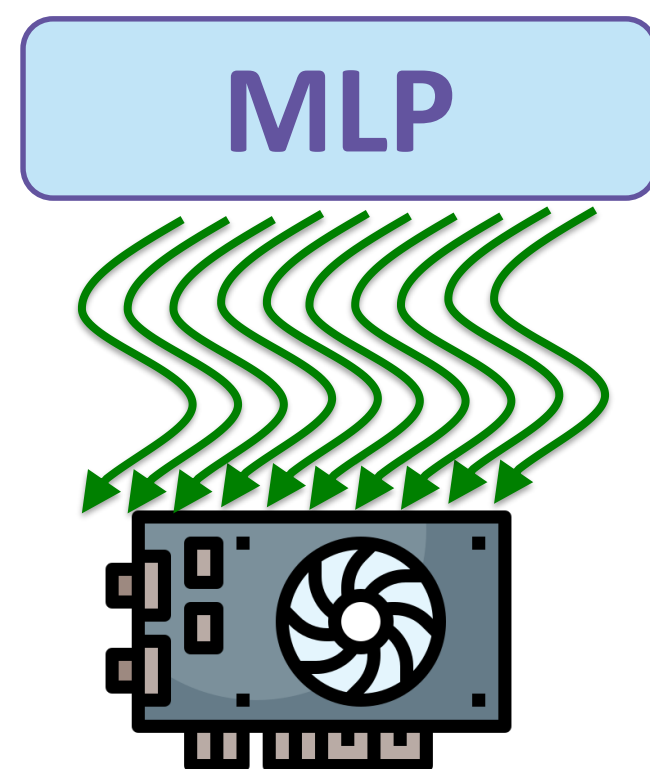
Fault Impact on ViT Operations

- We split the ViT into its essential operations
- We measure the FIT rate of each operation



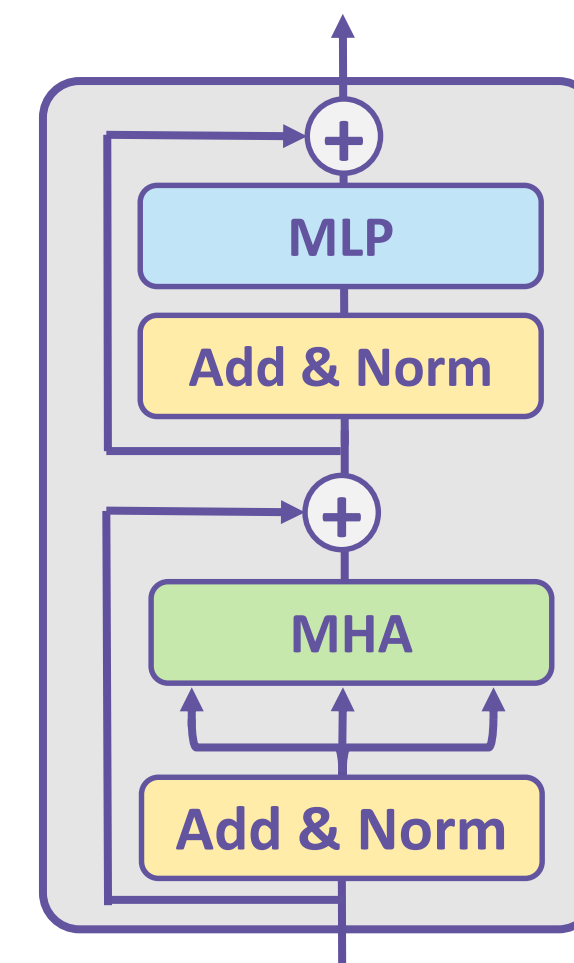
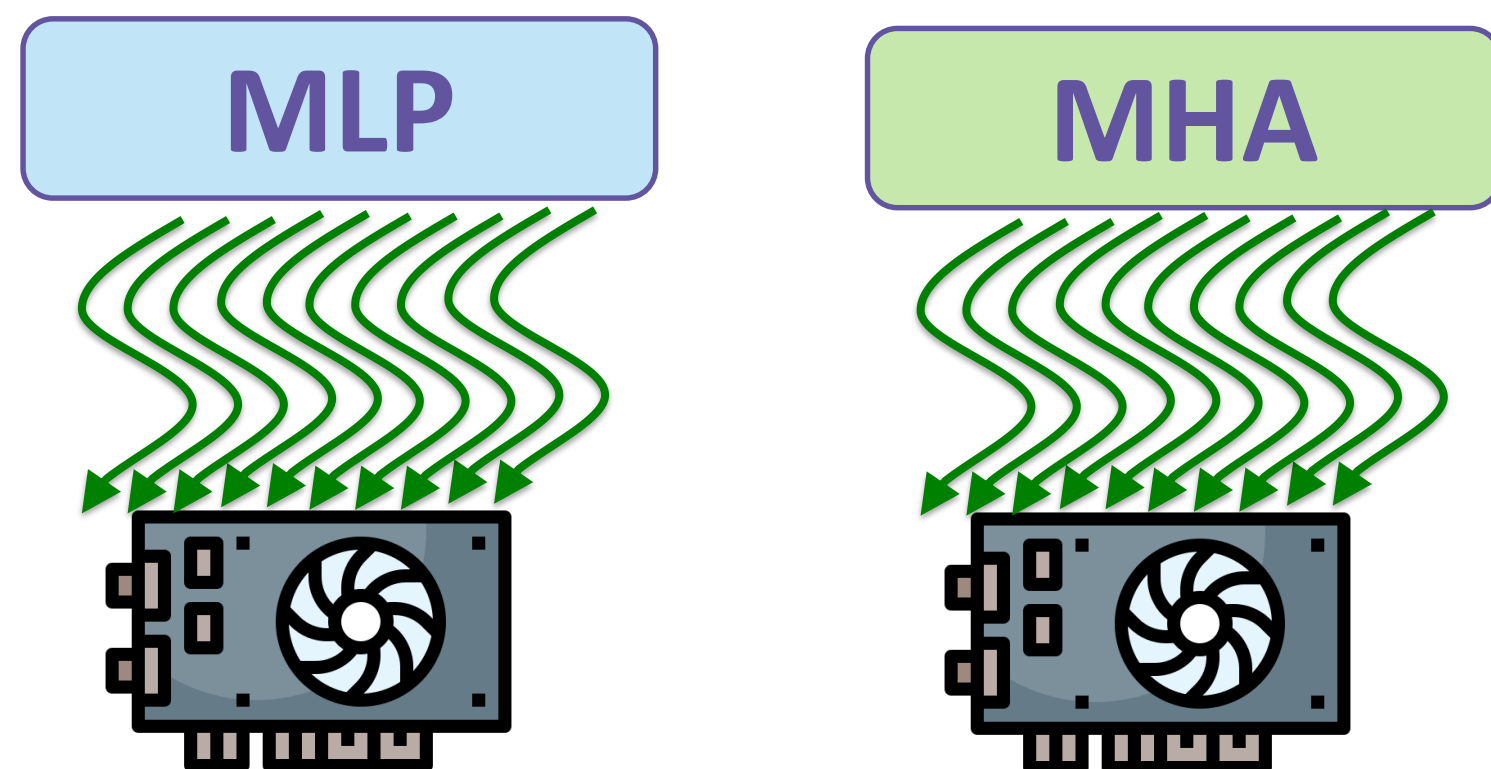
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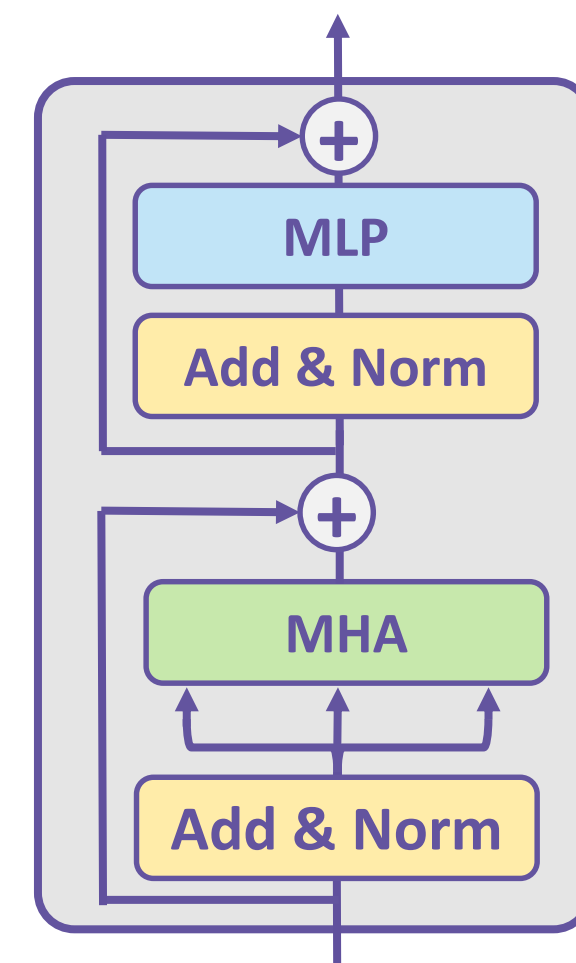
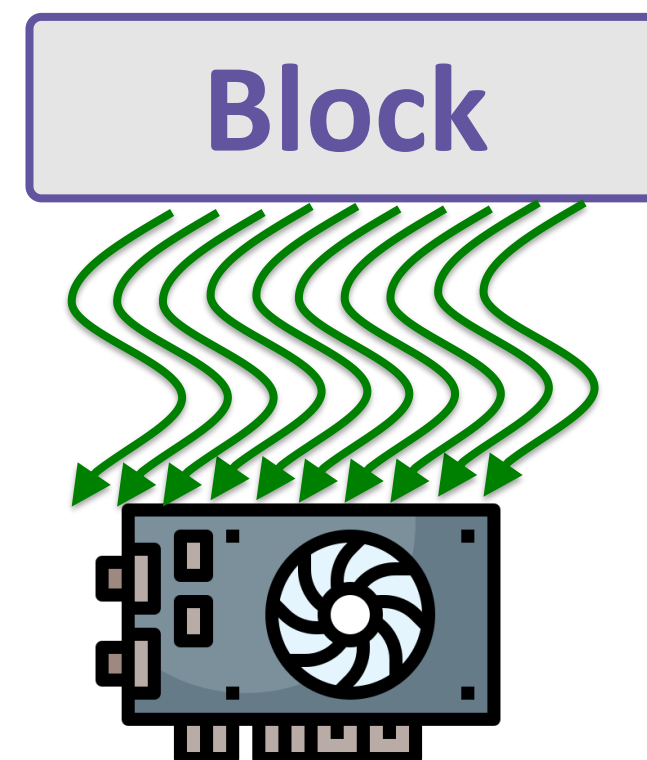
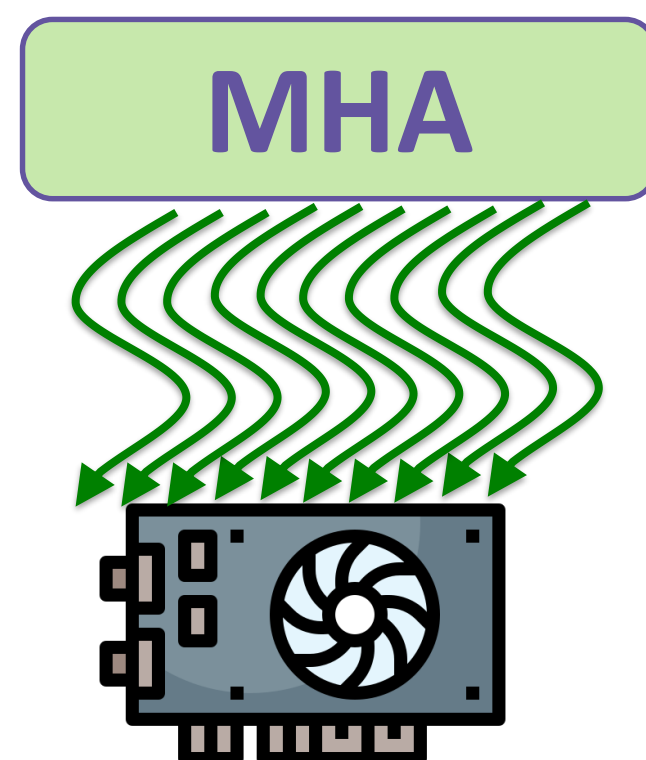
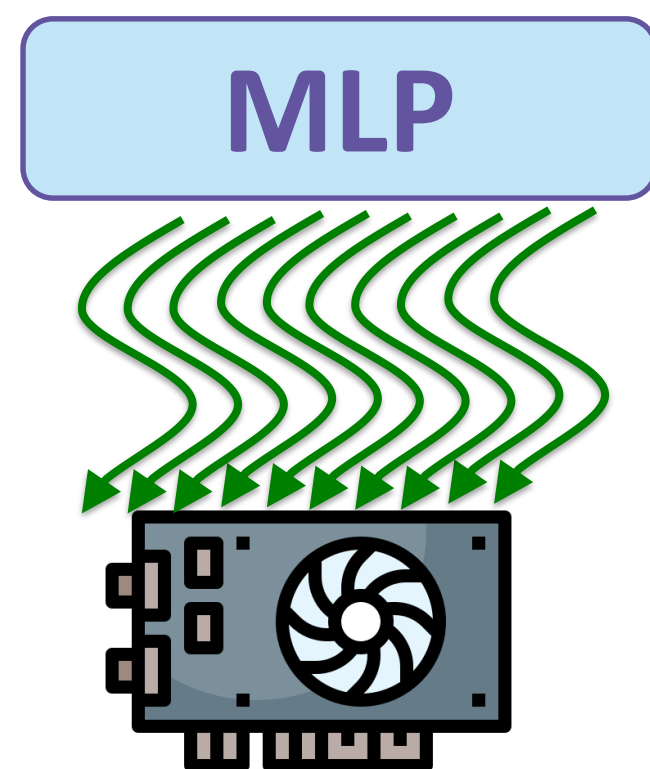
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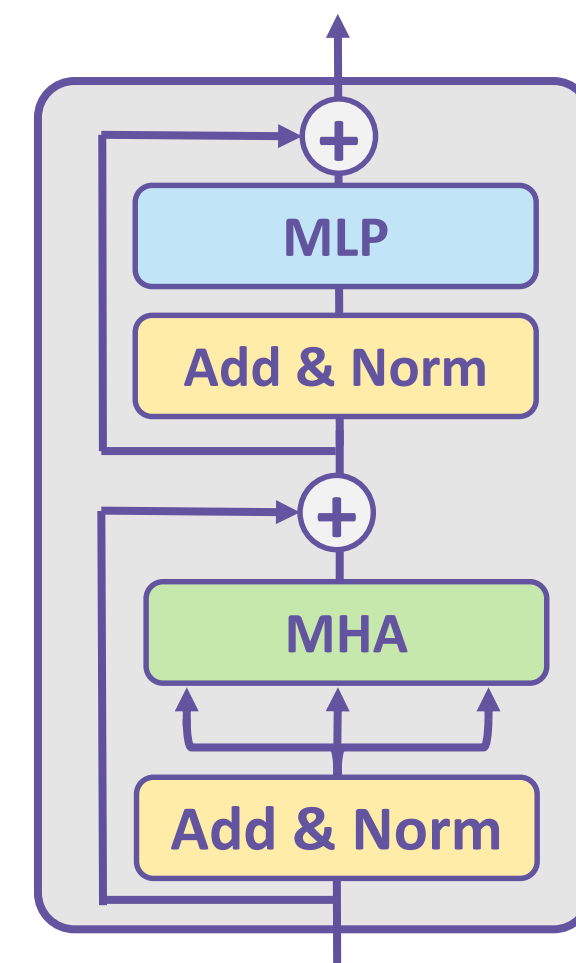
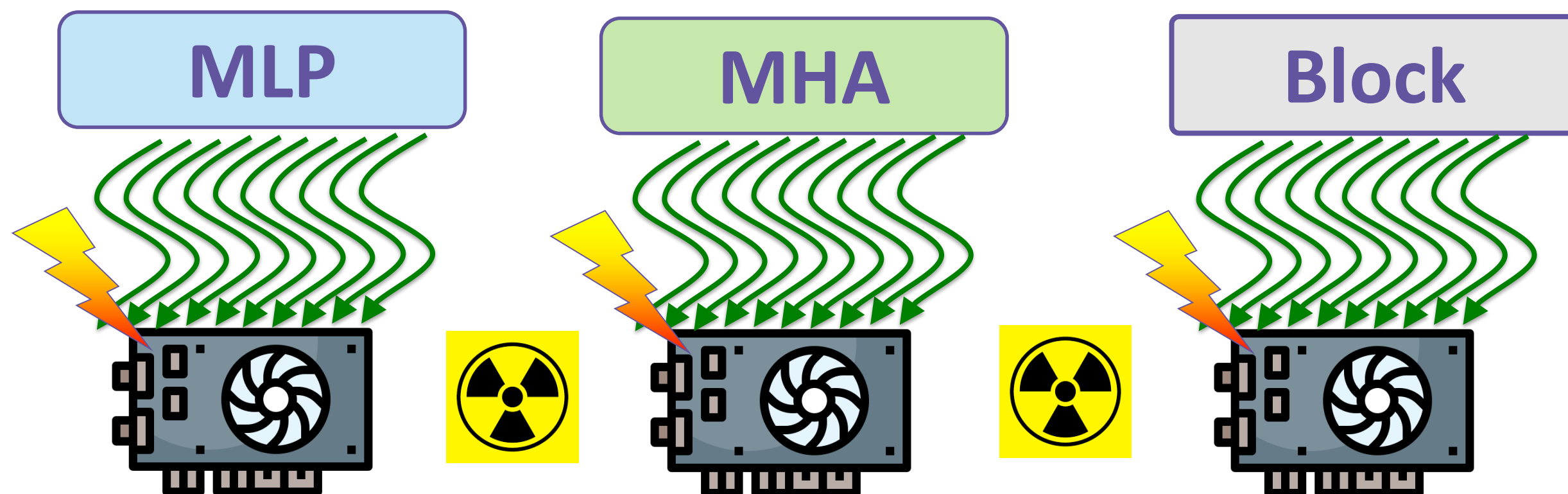
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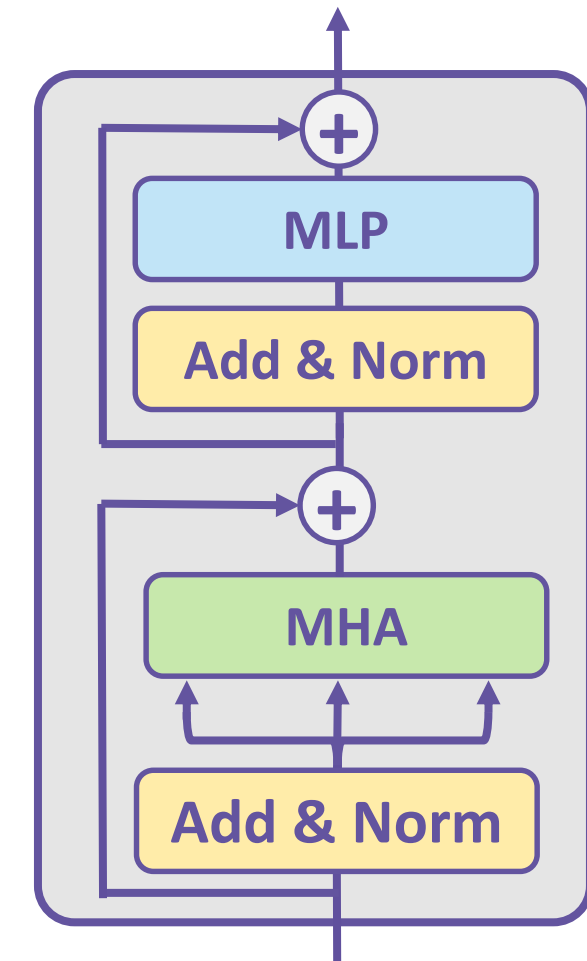
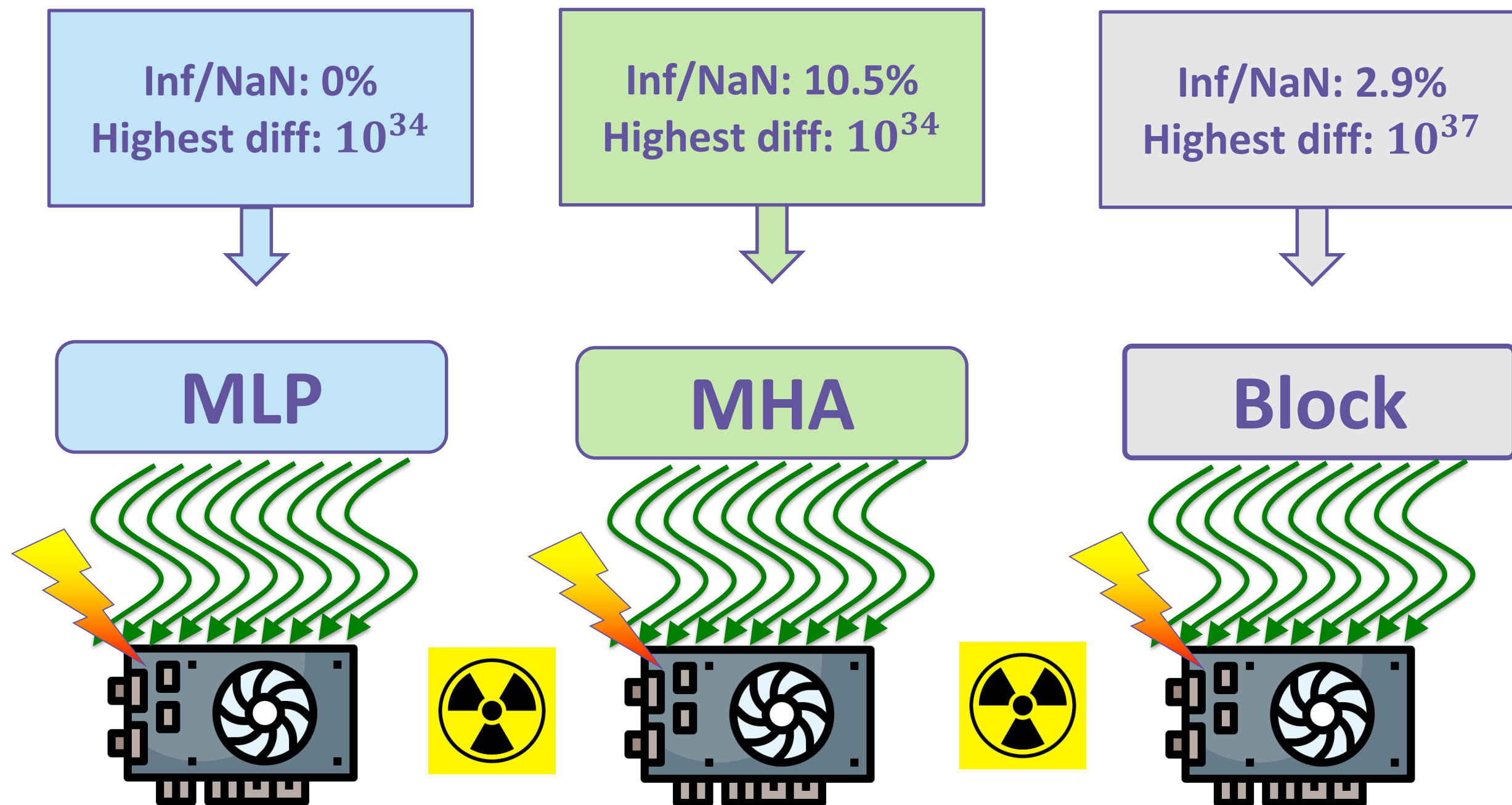
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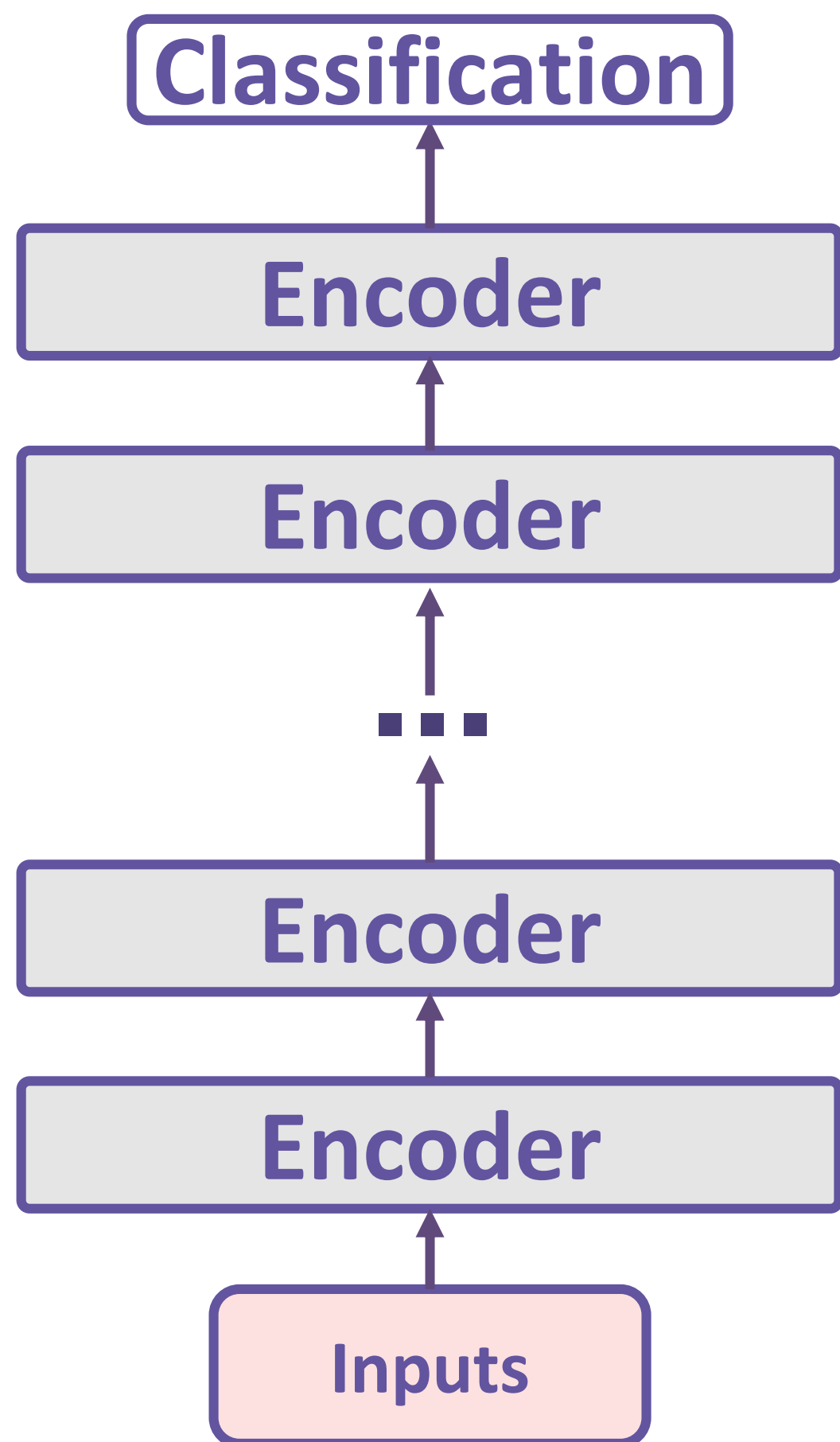


Fault Impact on ViT Operations

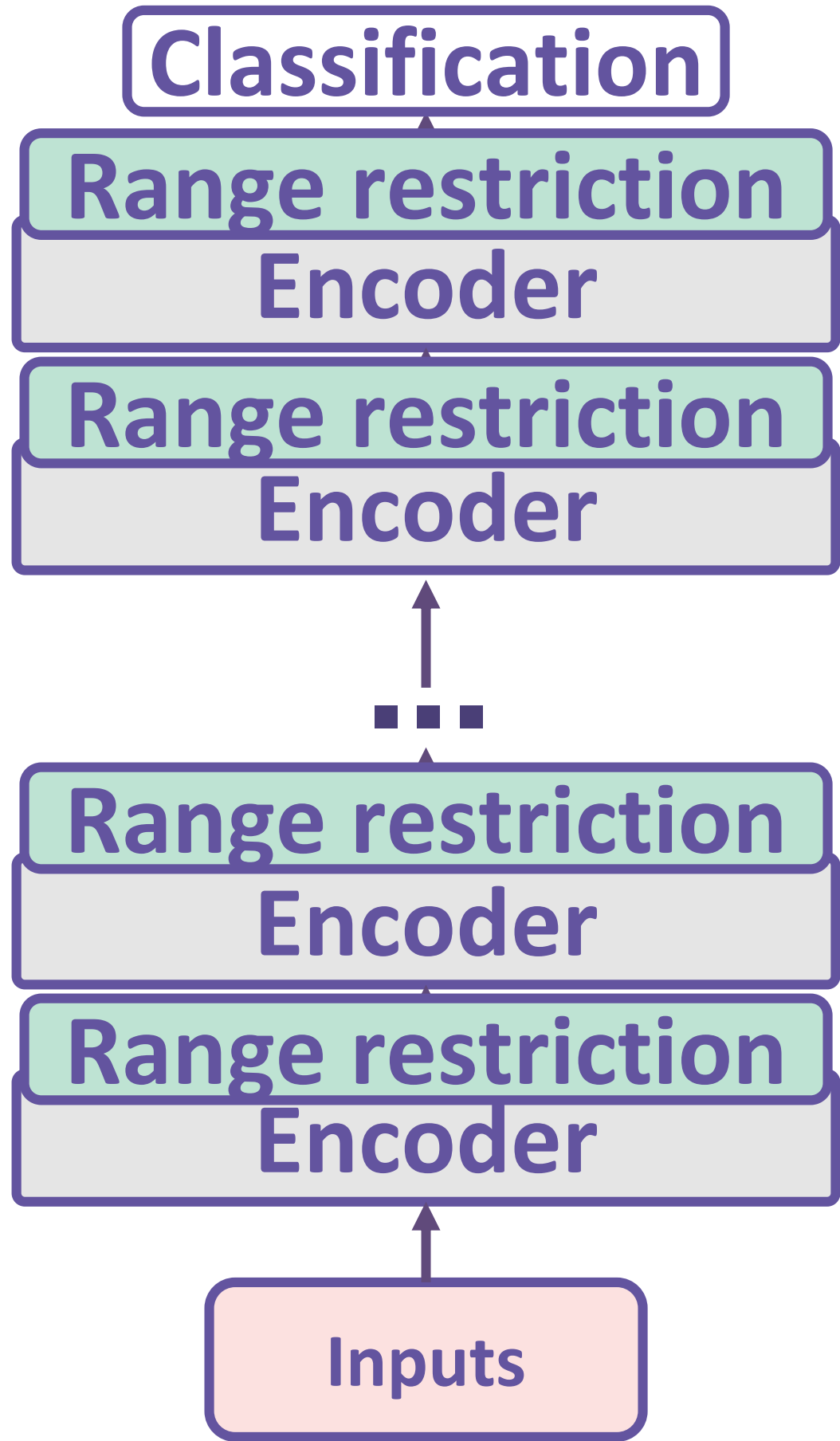
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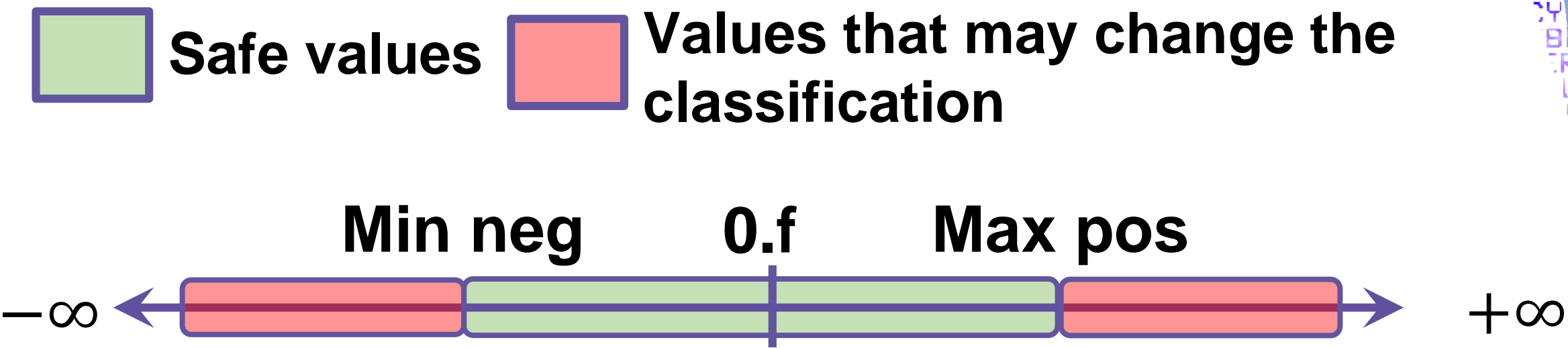
MaxiMals Idea



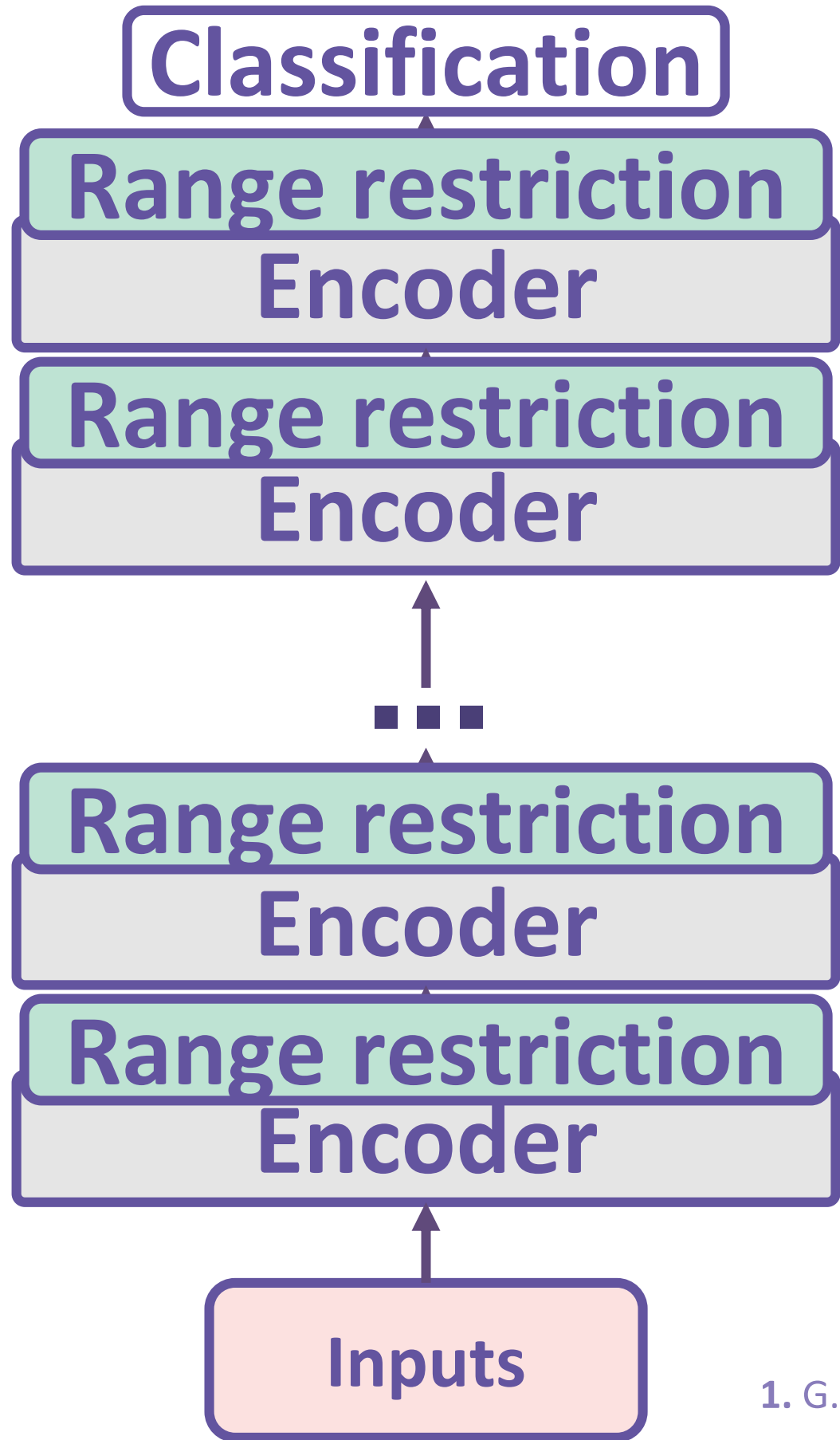
MaxiMals Idea



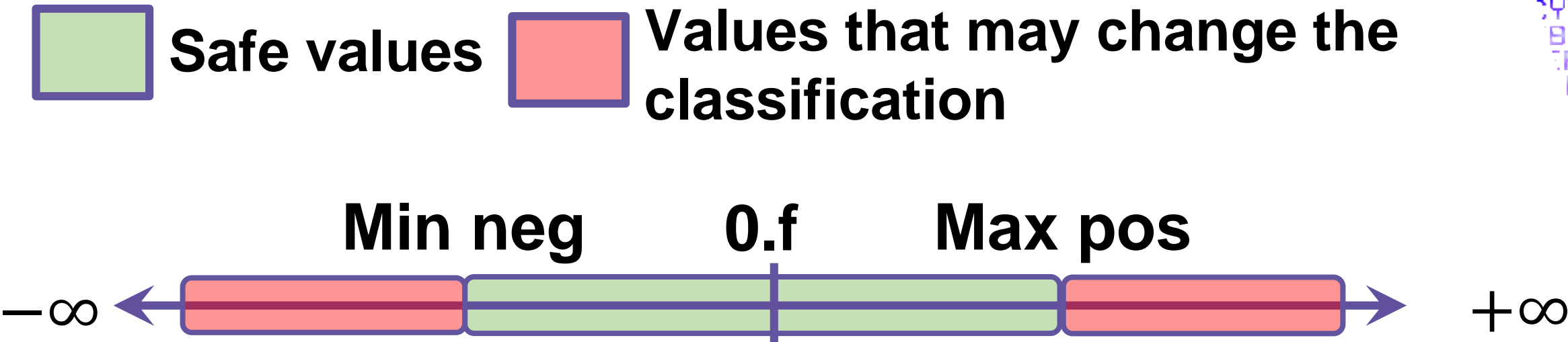
Range Restriction is a common hardening technique



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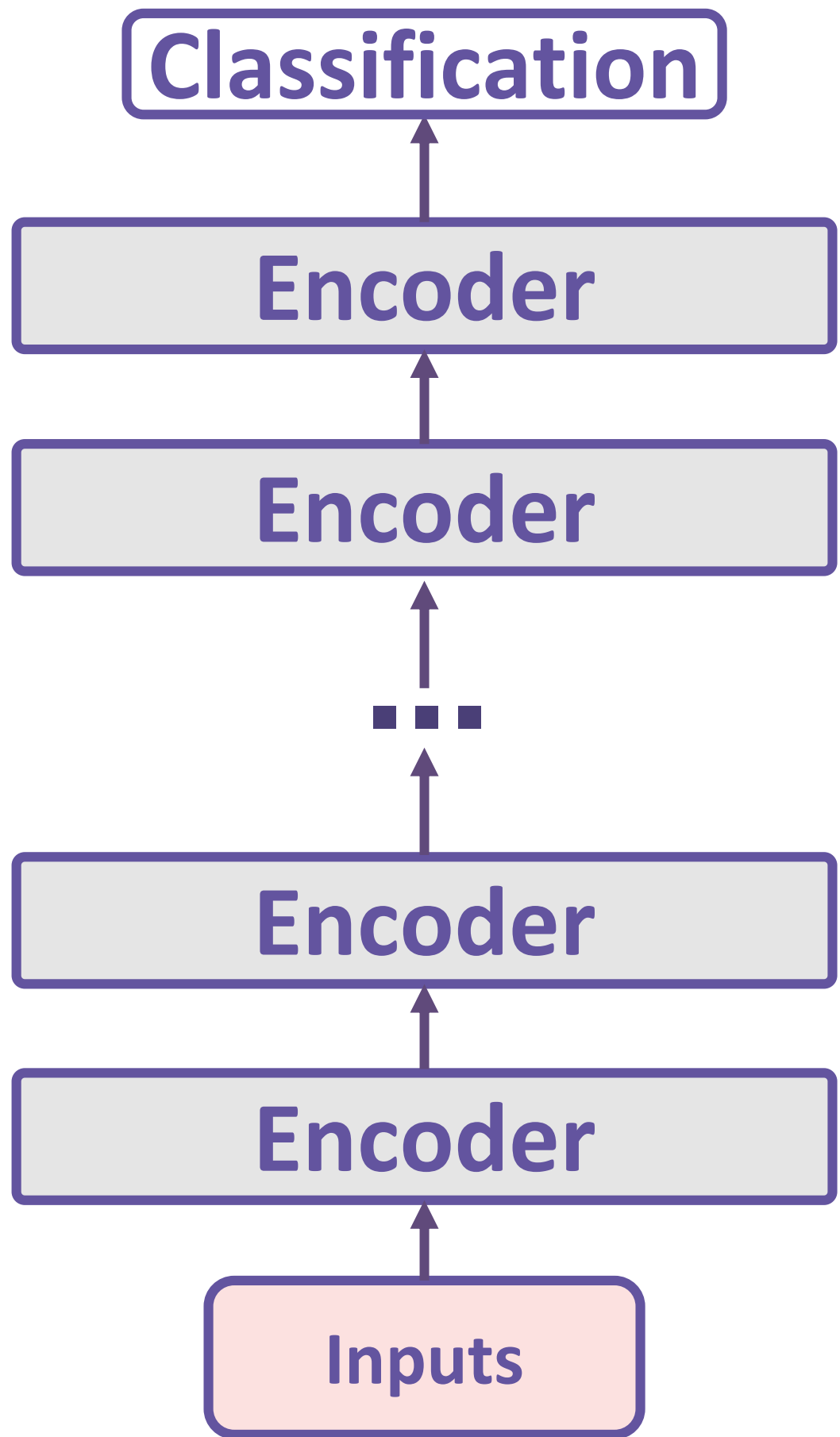


68% time overhead if applied on every block of a ViT¹

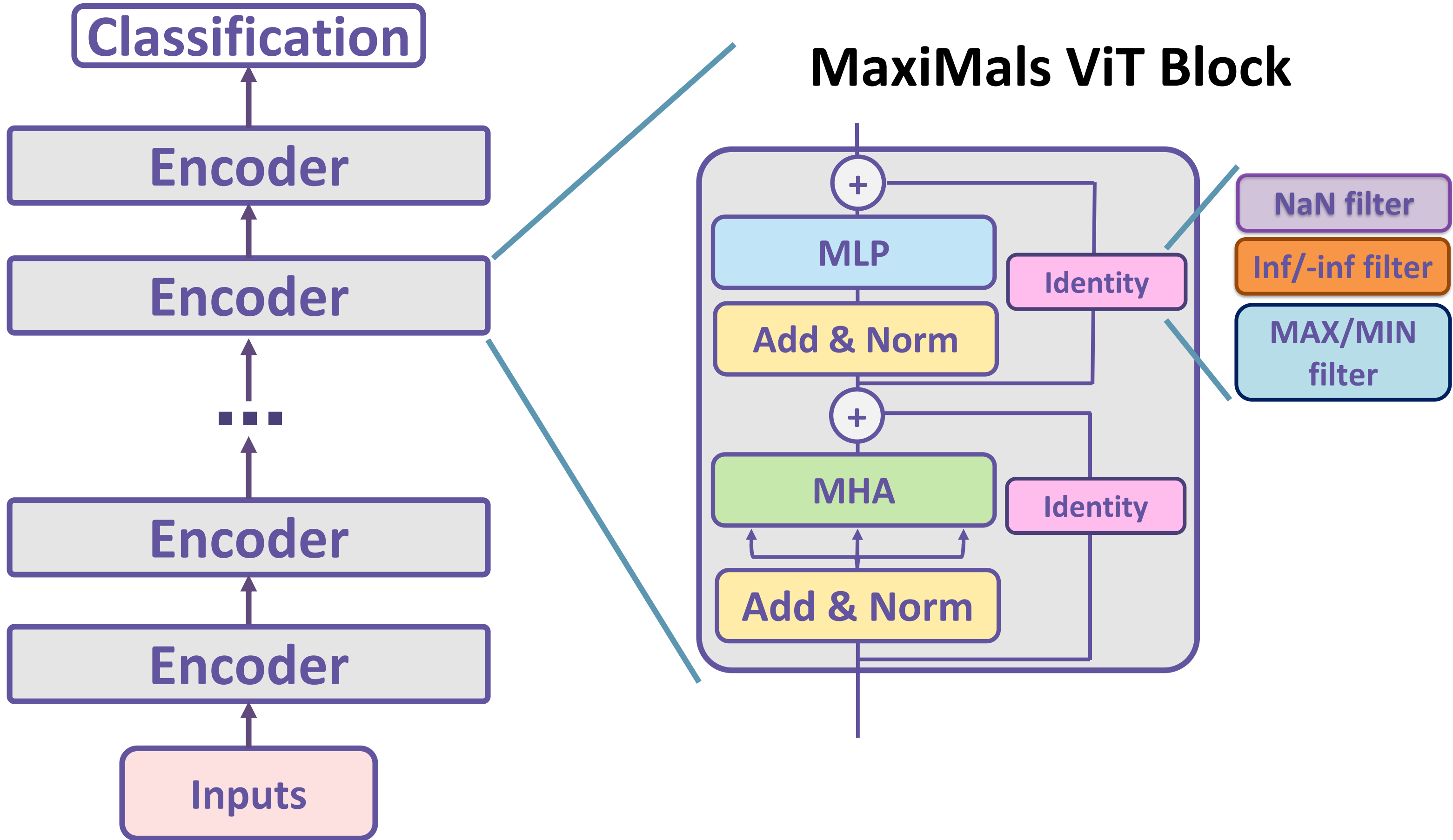


1. G. Gavarini *et al.* "Evaluation and mitigation of faults affecting swin transformers" – IEEE IOLTS 2023

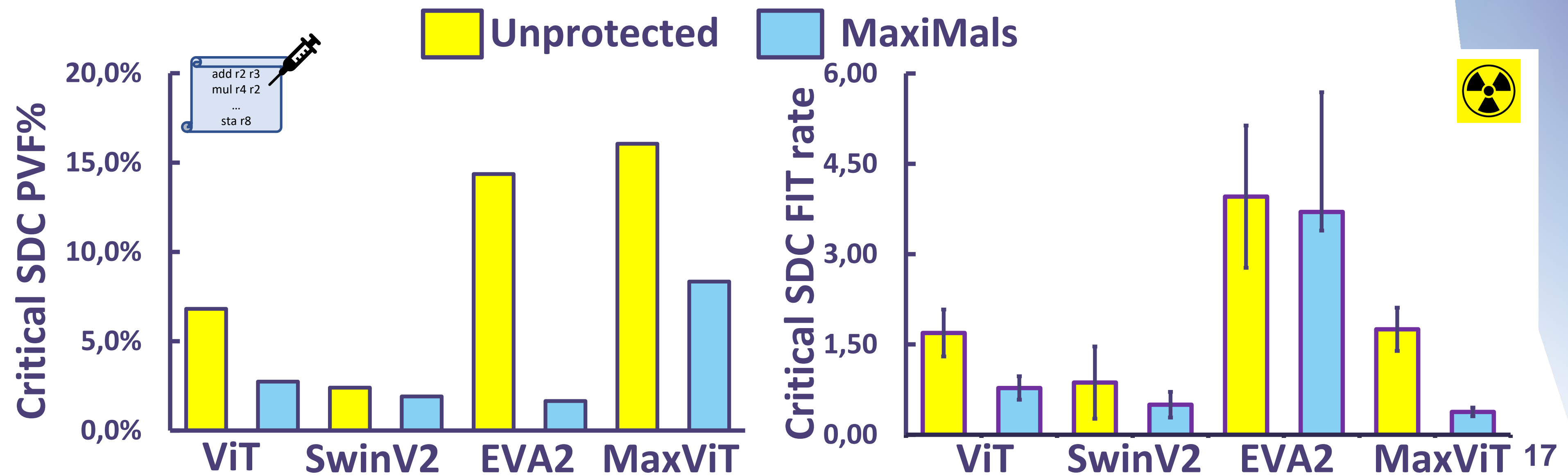
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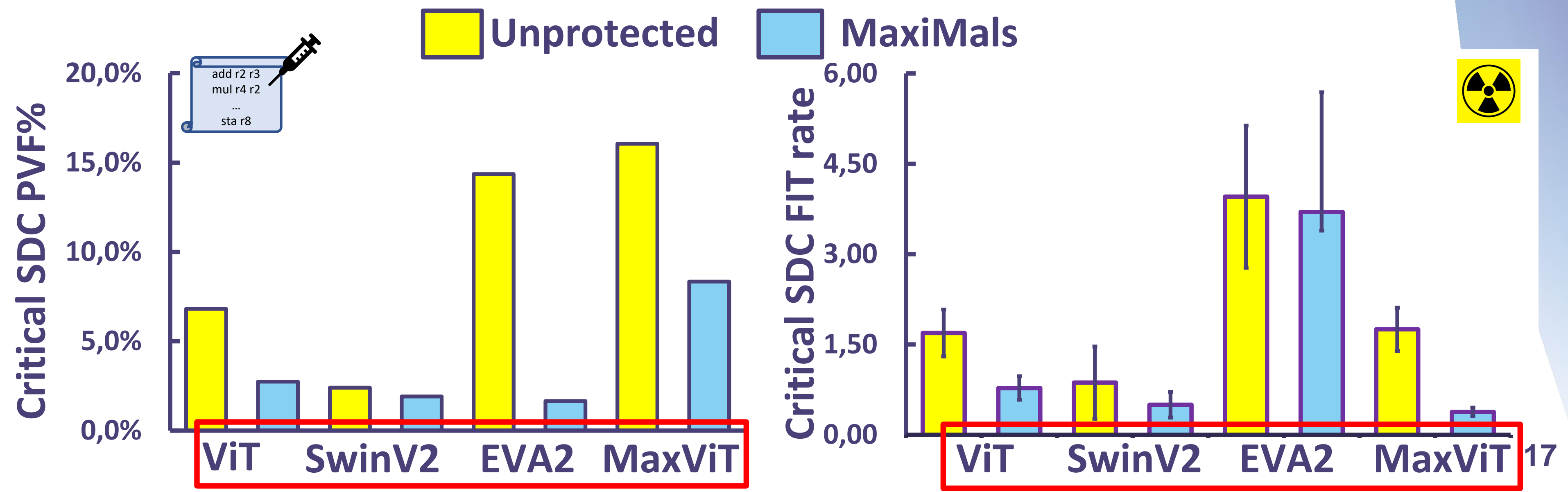
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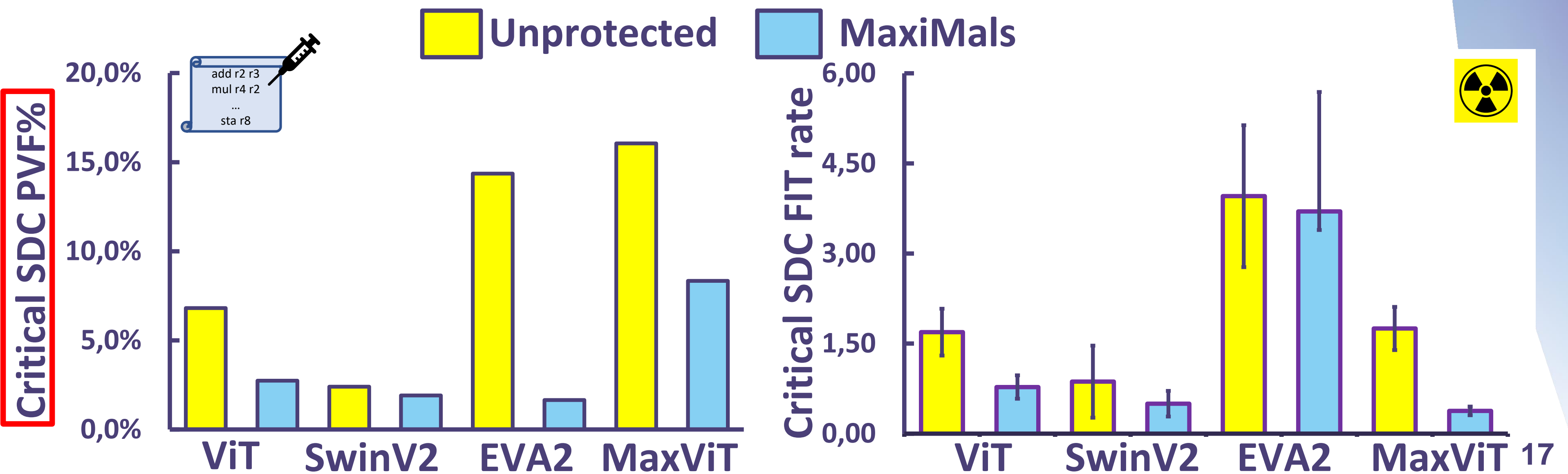
MaxiMals Efficiency



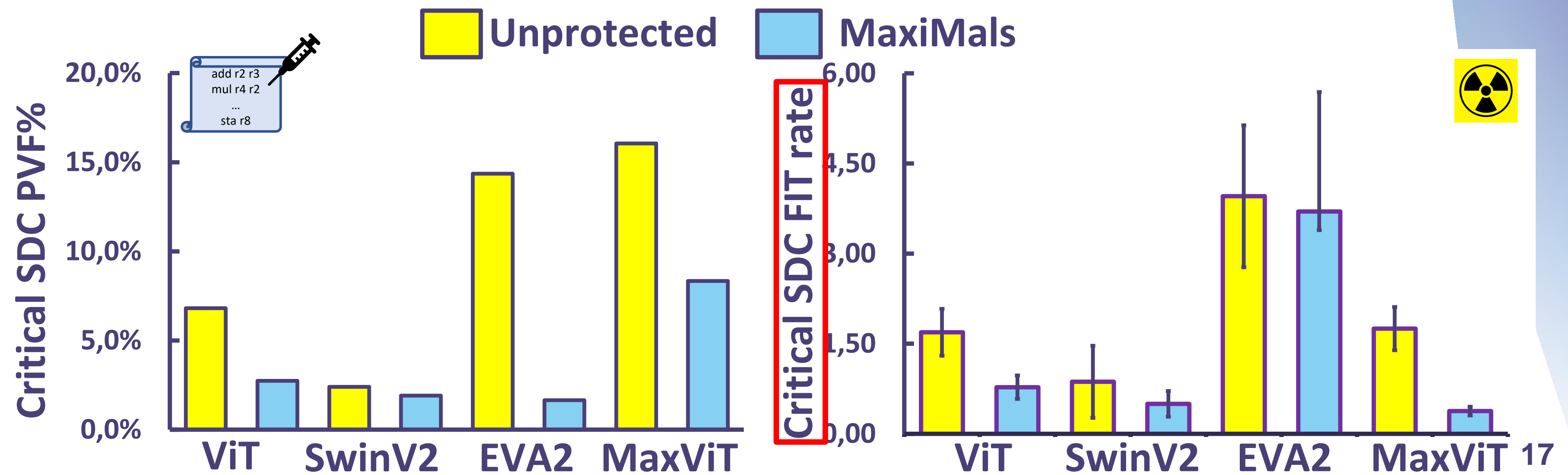
MaxiMals Efficiency



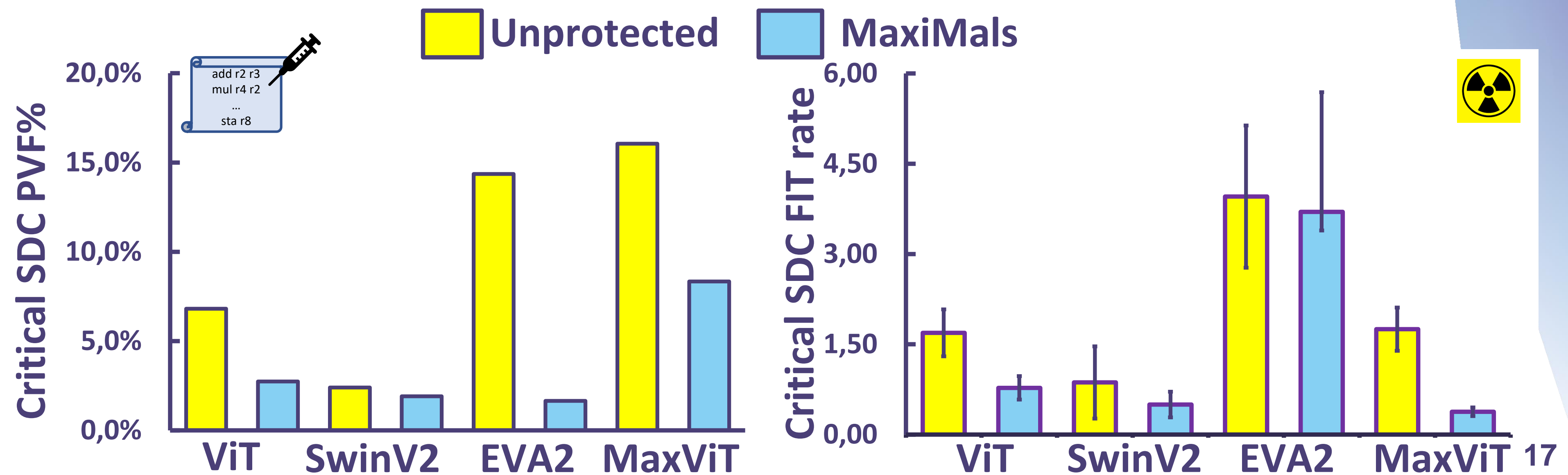
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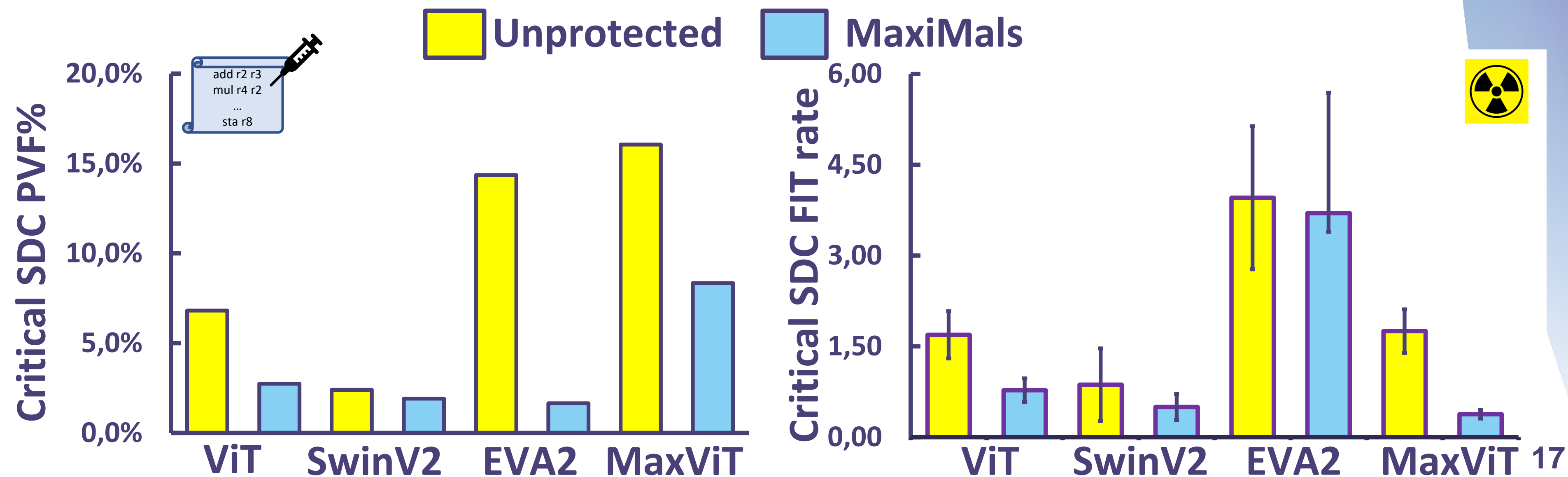
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MaxiMals Efficiency

Fault Simulation:

- Injections with NVBitFI
- Critical SDCs are reduces by half on average



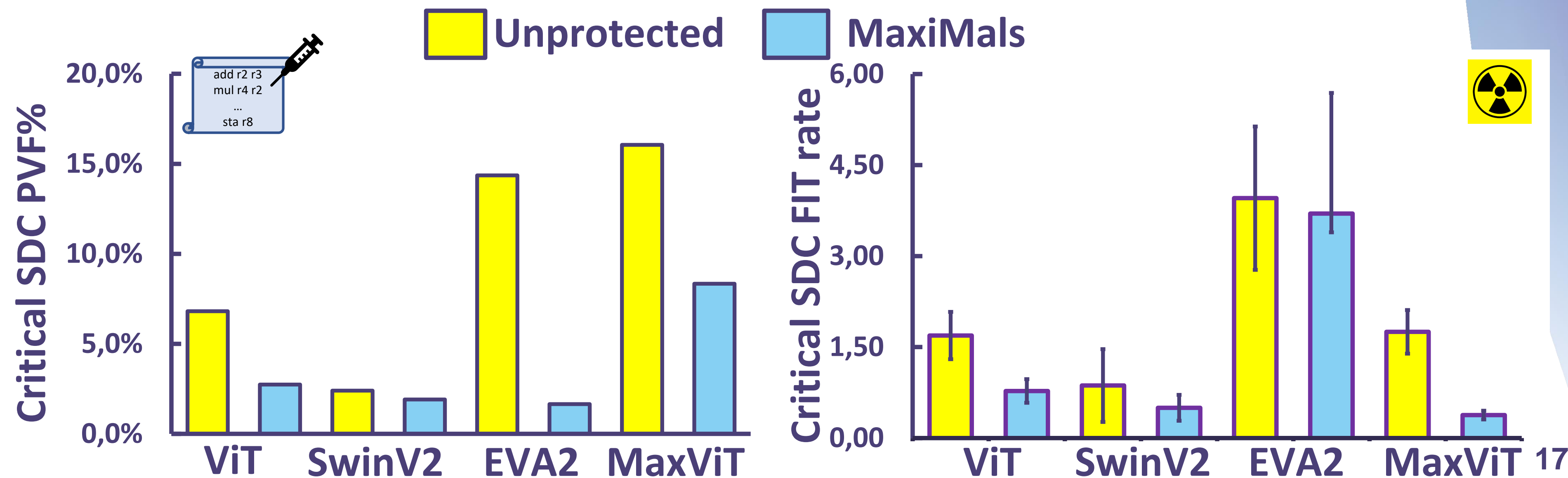
MaxiMals Efficiency

Fault Simulation:

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Beam injection:

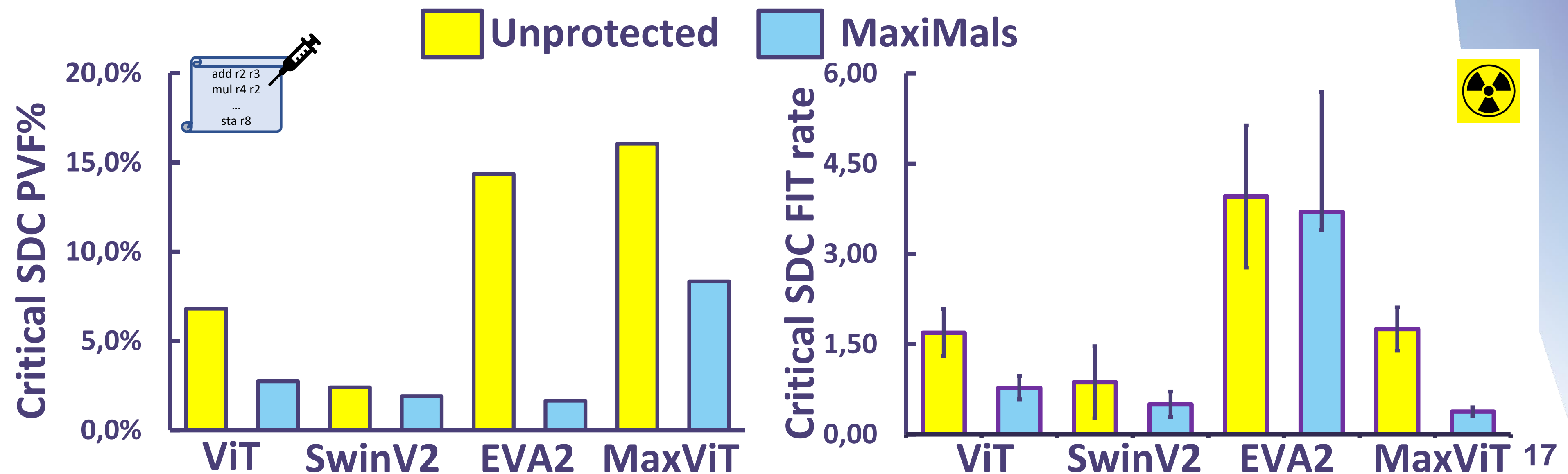
- Similar reduction for beam
- Similar trend on FP16



MaxiMals Efficiency

Average overhead:

- Time: 7.1% (max. 16.1%)
- 1.6% of additionnal instructions



Conclusions and Future Directions



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Conclusions and Future Directions

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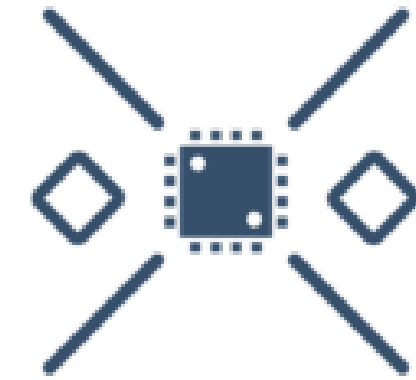
Future works

- Impact on INT8 precision
- Dependency between inputs and Critical SDC rates
- Other fault models:
 - different particles
 - HW attacks (RowHammer)

Supporters



Science & Technology
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Avec le soutien de





Thank you !

Questions ?

Contact: lucas.roquet@inria.fr